STM32 ????? VESC ???? (4WD_RWD ????)

? ??????????

□□□□□ STM32 □□□□□□□□□□ CAN Bus □ 4 □ VESC □□□□□□□□□□□□□□□□□□□□□

- □□□□ □ 48V □□□□□□□□□□□ 1000W□
- □□□□ □ 250W □□□□ (□□□□□□□□□□□□□□□)□
- □□□□ □ 500W □□□□□
- □□□□ □ 20 □□□□□□□ 1:4.4□□□□□□□ 390 RPM□
- □□□□ □□□ 4WD □□ (1:2 □□□□) $\rightarrow$ □□□□□ RWD □□ (□□□□□□□□□□□□□□)□

?? ?????????? (Bug Fixes & Improvements)

1. □□ **CAN** □□□□□ □□ `VESC_Send_Current` □□ Timeout □□□□□ VESC □□□□□□□□□ MCU □□□□□
2. □□□□□□□ **(Buffer Overflow)**□□ `ENCODE_Step_up` □□ `TxData` □□□□□ 8 bytes □□□□ DLC (Data Length Code) □□□
3. □□ **ADC** □□ □□ `HAL_ADC_Start()` □□ `while(1)` □□□□□□□□□□□□□ (Continuous Mode) □□□□□□
4. □□ **CAN ID** □□□□ □□□□□□ `HAL_CAN_RxFifo0MsgPendingCallback` □□□ `StdId` □□□□□□□□□□□□□□□□□

? ????? (`main.c`)

```
/* USER CODE BEGIN Header */
/**
  ******************************************************************************
```

```
  * @file           : main.c
  * @brief          : STM32 VESC 4WD/RWD Dynamic Control System
  ******************************************************************************
  */
/* USER CODE END Header */
/* Includes ------------------------------------------------------------------*/
#include "main.h"

/* Private includes ----------------------------------------------------------*/
/* USER CODE BEGIN Includes */

/* USER CODE END Includes */

/* Private define ------------------------------------------------------------*/
/* USER CODE BEGIN PD */
#define POWER_LIMIT_W         1000    // □□□□□□□□ 1000W
#define BATTERY_VOLTAGE_NOM   48      // □□□□ 48V

// □□□□□□ (mA)
#define MAX_TOTAL_CURRENT_MA  ((POWER_LIMIT_W * 1000) / BATTERY_VOLTAGE_NOM)

// □□□□□□□ (ERPM: Electrical RPM)
// □□ 20□□□4.4□□□□390rpm □□□□□□□□
#define ERPM_THRESHOLD_RWD    10000
#define ERPM_HYSTERESIS       500     // □□□□□□□□□□□□□□□□□

// VESC CAN ID □□ (□□ VESC Tool □□□□)
#define VESC_ID_FRONT_L       0xB1
#define VESC_ID_FRONT_R       0xB2
#define VESC_ID_REAR_L        0xA3
#define VESC_ID_REAR_R        0xA4

// VESC CAN Packet ID
#define CAN_PACKET_SET_CURRENT 1
#define ENCODER_RESOLUTION     4096.0f
#define STEERING_CENTER_OFFSET 2048.0f
/* USER CODE END PD */

/* Private variables ---------------------------------------------------------*/
ADC_HandleTypeDef hadc1;
ADC_HandleTypeDef hadc2;
```

```
CAN_HandleTypeDef hcan1;

/* USER CODE BEGIN PV */
int32_t current_erpm = 0;       // □□□□ (□ CAN RX □□)
uint16_t throttle_adc = 0;      // 0 - 4095
int32_t target_total_ma = 0;    // □□□□□ (mA)
uint16_t speed_adc = 0;         // □□□□□ ADC

float current_angle = 0.0f;     // 0 ~ 360 □□□□□□□
float steering_angle = 0.0f;    // -180 ~ +180 □□□□□□□□□□
uint32_t raw_encoder_value = 0; // 0 ~ 4095
/* USER CODE END PV */

/* Private function prototypes -----------------------------------------------*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_ADC1_Init(void);
static void MX_CAN1_Init(void);
static void MX_ADC2_Init(void);

/* USER CODE BEGIN PFP */
// □□□□□□□□□□□□□□□
typedef enum {
    MODE_4WD,
    MODE_RWD
} DriveMode_t;

DriveMode_t drive_mode = MODE_4WD;

void VESC_Send_Current(uint8_t controller_id, int32_t current_ma);
void Control_Loop_1kHz(void);
void ENCODE_Step_up(void);
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan);
/* USER CODE END PFP */

/**
  * @brief  The application entry point.
  */
int main(void)
{
  /* MCU Configuration--------------------------------------------------------*/
```

```
  HAL_Init();
  SystemClock_Config();

  /* Initialize all configured peripherals */
  MX_GPIO_Init();
  MX_ADC1_Init();
  MX_CAN1_Init();
  MX_ADC2_Init();

  /* USER CODE BEGIN 2 */
  // □□ CAN □□□□□
  HAL_CAN_Start(&hcan1);
  HAL_CAN_ActivateNotification(&hcan1, CAN_IT_RX_FIFO0_MSG_PENDING);

  // □□□□□ ADC □□□□ while(1) □□□□□□□□□□□□□□□□□□□□
  HAL_ADC_Start(&hadc1);
  HAL_ADC_Start(&hadc2);
  /* USER CODE END 2 */

  /* Infinite loop */
  while (1)
  {
      Control_Loop_1kHz();
  }
}

/* USER CODE BEGIN 4 */

/**
 * @brief □□□□□ (□□□□□□□□□ CAN □□ Bus □□)
 */
void Control_Loop_1kHz(void)
{
    static uint32_t last_control_tick = 0;
    uint32_t current_tick = HAL_GetTick();

    // □□□□□□ (□□ 500Hz)
    if ((current_tick - last_control_tick) < 2) {
        return;
    }
    last_control_tick = current_tick;
```

```
// 1. □□□□□□□□□□ ADC (0 ~ 4095)
throttle_adc = HAL_ADC_GetValue(&hadc1);
speed_adc = HAL_ADC_GetValue(&hadc2);
current_erpm = ((int32_t)speed_adc * 20000) >> 12;

// 2. □□□□□□□□ (mA)
target_total_ma = ((int32_t)throttle_adc * MAX_TOTAL_CURRENT_MA) >> 12;

// 3. □□□□ (□□ ERPM □□□)
int32_t abs_erpm = (current_erpm < 0) ? -current_erpm : current_erpm;

if (drive_mode == MODE_4WD) {
    if (abs_erpm > (ERPM_THRESHOLD_RWD + ERPM_HYSTERESIS)) {
        drive_mode = MODE_RWD;
    }
} else {
    if (abs_erpm < (ERPM_THRESHOLD_RWD - ERPM_HYSTERESIS)) {
        drive_mode = MODE_4WD;
    }
}

// 4. □□□□□□ (Distribute Current)
int32_t current_front_axle = 0;
int32_t current_rear_axle = 0;

if (drive_mode == MODE_4WD) {
    // --- 4WD □□ (□□□□□) ---
    // □ 250W + □ 500W□□□ 1:2
    current_front_axle = target_total_ma / 3;
    current_rear_axle  = target_total_ma - current_front_axle;
} else {
    // --- RWD □□ (□□□□) ---
    // □□□□ (□□□□□□□□)□□□□□□□□
    current_front_axle = 0;
    current_rear_axle  = target_total_ma;
}

// 5. □□□□□□□□□□□□□□
int32_t cmd_front = current_front_axle / 2;
int32_t cmd_rear  = current_rear_axle / 2;
```

```
    static uint8_t motor_step = 0;
    switch(motor_step)
    {
        case 0:
            VESC_Send_Current(VESC_ID_FRONT_L, cmd_front);
            motor_step = 1;
            break;
        case 1:
            VESC_Send_Current(VESC_ID_FRONT_R, cmd_front);
            motor_step = 2;
            break;
        case 2:
            VESC_Send_Current(VESC_ID_REAR_L,  cmd_rear);
            motor_step = 3;
            break;
        case 3:
            VESC_Send_Current(VESC_ID_REAR_R,  cmd_rear);
            motor_step = 0;
            break;
    }
}

/**
 * @brief □□□□□□□□ VESC
 */
void VESC_Send_Current(uint8_t controller_id, int32_t current_ma)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[4];

    TxHeader.ExtId = (CAN_PACKET_SET_CURRENT << 8) | controller_id;
    TxHeader.IDE = CAN_ID_EXT;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;

    // Big Endian packing
    TxData[0] = (uint8_t)(current_ma >> 24);
    TxData[1] = (uint8_t)(current_ma >> 16);
    TxData[2] = (uint8_t)(current_ma >> 8);
```

```
    TxData[3] = (uint8_t)(current_ma);

    // □□□□□□□□□ Timeout □□□□□□ CAN Bus □□□□□□□□
    uint32_t timeout = 0;
    while (HAL_CAN_GetTxMailboxesFreeLevel(&hcan1) == 0)
    {
        timeout++;
        if (timeout > 50000) {
            return; // □□□□□□□□□□□□
        }
    }
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
}

/**
 * @brief □□□□□□□□□□
 */
void ENCODE_Step_up(void)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[8]; // □□□□□□□□□ 8 bytes □□□□

    TxHeader.StdId = 0x01;
    TxHeader.IDE = CAN_ID_STD;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;

    TxData[0] = 0x04;
    TxData[1] = 0x01;
    TxData[2] = 0x04;
    TxData[3] = 0xAA;
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);

    // □□□□□
    TxHeader.DLC = 5; // □□□□□□□□□ 5
    TxData[0] = 0x05;
    TxData[1] = 0x01;
    TxData[2] = 0x05;
    TxData[3] = 0x88;
    TxData[4] = 0x13; // □□□ 5 □ byte □□□□
```

```
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
}

/**
 * @brief CAN □□□□□□□□□ (□□□□□□□□□□)
 */
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan)
{
    CAN_RxHeaderTypeDef RxHeader;
    uint8_t RxData[8];

    if (hcan->Instance == CAN1)
    {
        if (HAL_CAN_GetRxMessage(hcan, CAN_RX_FIFO0, &RxHeader, RxData) == HAL_OK)
        {
            // □□□□□□□□□□□□□ ID□□□ VESC □□□□□□□
            if (RxHeader.IDE == CAN_ID_STD && RxHeader.StdId == 0x01)
            {
                if (RxData[0] == 0x07 && RxData[1] == 0x01 && RxData[2] == 0x01)
                {
                    // □□□□□
                    raw_encoder_value = (uint32_t)((RxData[6] << 24) |
                                                   (RxData[5] << 16) |
                                                   (RxData[4] << 8)  |
                                                    RxData[3]);
                }
            }
        }
    }
}
/* USER CODE END 4 */
```

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