STM32 ??????

STM32 Nucleo-F446RC + CubeMX ??????

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□□□□ **STM32** □□□□□□□□□□□□ □□□□□□□□ **13** □□□ □□□□□□□□□□□□ RTOS □□□□□□□□□□

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> □□ □□□□ □□□ 3-5 □□
> □□ □□□□ □ STM32 Nucleo-F446RC × 1
> □□ □□□□ □ STM32CubeIDE□□□□

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? ?????? 1-3 ??

□□□□□□□□□□□□□□□□□□□□ GPIO □□□□□

□□	□□	□□□□	□□
□ 1 □	□□□□ **+ Blink LED**	IDE □□□□□□□□□ GPIO □□	□
□ 2 □	□□□□□□	□□□□ PLL □□□ Clock Tree	□□
□ 3 □	**GPIO** □□	□□□□□□□□□□□□	□□

? ???????? 4-9 ??

□□ STM32 □□□□□□□□□□□□□□□□□□

□□	□□	□□□□	□□

□ 4 □	**UART □□□□**	□□□□□□□□□□□□□ □	□□
□ 5 □	**ADC □□□□**	□□□□□□□□□□□□□ □□	□□
□ 6 □	**□□□ + PWM**	□□□□□ PWM □□□□□□□□	□□
□ 7 □	**DMA □□**	□ CPU □□□□□□□□□□□□	□□□
□ 8 □	**I2C □□**	□□□□□□□□□□□□□ □□	□□□
□ 9 □	**SPI □□□□**	□□□□□□□□□□ SD □□□	□□□

? ????????? 10-12 ??

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□ 10 □	**CANbus □□**	□□□□□□□□□□□□□ □□	□□□
□ 11 □	**RS-485 □□**	□□□□□□□□□□□□□	□□□
□ 12 □	**□□ CRC □□**	□□□□□□□□□□□□□ □□	□□

? ???????? 13 ??

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□ 13 □	**FreeRTOS**	□□□□□□□□□□□□□ □□	□□□□

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? 1 ?????? + Blink LED

□□ □ `stm32_lesson_01_env.md`

□ □□□□□

- □□ STM32CubeIDE □□□□□
- □□ Nucleo-F446RC □□
- □□□□□ LED □□□□

□□ □□□□□

- STM32CubeIDE □□□□□□□
- □□□□□□□□□□
- CubeMX □□□□□□
- □□□□□□ Blink LED □□

? 2 ????????

□□ □ `stm32_lesson_02_clock.md`

□ □□□□□

- □□ STM32F446 □□□□□
- □□ Clock Tree □□
- □□□□□□□□ LED □□□□

□□ □□□□□

- □□□□ HSI□ HSE□□ PLL □□□□
- □□□□□□ AHB□ APB1□ APB2□□□
- Clock Tree □□□□
- □□□□□□□□□

? 3 ??GPIO ????????????

□□ □ `stm32_lesson_03_gpio_adv.md`

□ □□□□□

- □□ GPIO □□□□
- □□□□□□□□□ EXTI□
- □□□□□□□□□

□□ □□□□□

- GPIO □□□□□□□□
- □□ /□□□□□□□

- □□□□□□□□
- □□□□□□□□□

? 4 ??UART ???? + ????

□□ □ `stm32_lesson_04_uart.md`

□ □□□□□

- □□ UART □□□□
- □□□□□□
- □□□□□□□□□□□

□□ □□□□□

- UART □□□□□□□□□□□□
- □□□□□□□□□
- □□□□□□□□□
- □□□□□□□□

? 5 ??ADC???????????? Channel?

□□ □ `stm32_lesson_05_adc.md`

□ □□□□□

- □□ ADC □□□□
- □□□□□□□□
- □□□□□□□

□□ □□□□□

- 12-bit ADC □□□□□□□□
- □□□□□□□□□□□□
- □□□□□□□□□□□
- □□□□□□□□□□□□□

? 6 ????? Timer + PWM

□□ □ `stm32_lesson_06_timer_pwm.md`

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- □□□□□□□□□
- □□ PWM □□□□
- □□□□□□□

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- □□□□□□□□
- PWM □□□□□□□□
- PWM □□□□□□□□
- □□ LED □□□□□□

? 7 ??DMA??????????

□□ □ `stm32_lesson_07_dma.md`

□ □□□□□

- □□ DMA □□□□
- □□ ADC + DMA
- □□□□□□□□

□□ □□□□□

- DMA □ CPU □□□□□
- □□□□□□□□□
- DMA □□□□□□
- ADC + DMA □□□□□□

? 8 ??I2C ?? + DMA

□□ □ `stm32_lesson_08_i2c.md`

□ □□□□□

- □□ I2C □□
- I2C □□□□□
- I2C + DMA □□

□□ □□□□□

- I2C □□□□□□□
- SCL□ SDA □□□□□□□
- □□□□□□□□□□□□
- □□□□□□□□□

? 9 ??SPI ?? + DMA

□□ □ `stm32_lesson_09_13_summary.md` □□ 9 □□□□

□ □□□□□

- □□ SPI □□□□□□
- SPI □□□□□
- SPI + DMA □□

□□ □□□□□

- MOSI□ MISO□ SCK□ CS □□□
- □□□□□□□□□
- SPI □ I2C □□□
- □□□□□□□□□

? 10 ??CANbus ?????????

□□ □ `stm32_lesson_09_13_summary.md` □□ 10 □□□□

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- CAN □□□□
- □□□□□□
- □□□□□□□□

□□ □□□□□

- CAN □□□□□□□□□
- CAN □□□ TJA1050 □□
- □□□□□□□□□□
- Nucleo ↔ Nucleo □□□□

? 11 ??RS-485 ??

□□ □ `stm32_lesson_09_13_summary.md` □□ 11 □□□□

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- RS-485 □□□□
- □□□□□□□

- □□□□□□□

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- RS-485 □ RS-232 □□□
- MAX485 □□□□□
- □□ /□□□□□□
- □□□□□□□

? 12 ???? CRC ??

□□ □ `stm32_lesson_09_13_summary.md` □□ 12 □□□□

□ □□□□□

- CRC □□□□
- STM32 □□ CRC
- □□□□□□□

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- CRC □□□□□□□□
- □□ vs □□ CRC □□□□
- CRC-32 □□□□□
- □□□□□□□□□

? 13 ??RTOS????????

□□ □ `stm32_lesson_09_13_summary.md` □□ 13 □□□□

□ □□□□□

- RTOS □□□□
- FreeRTOS □□
- □□□□□

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- □□□□□□□□□
- □□□□□□□□□□□□□□□□□□
- FreeRTOS □□ API
- □□□□□□□□□

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- [ ] □□ STM32CubeIDE□ v1.13+□□ https://www.st.com/en/development-tools/stm32cubeide.html
- [ ] □□□□□□□ 13 □ Markdown □□
- [ ] □□ STM32 Nucleo-F446RC □□□
- [ ] □□ USB Type-B □□□
- [ ] □□ ST-Link □□□□□□□□□□

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```
□ 1 □□□□□□ ✓
   ↓
□ 2 □□□□□□ ✓
   ↓
□ 3 □□GPIO □□ ✓
   ↓
□ 4 □□UART □□ ✓
   ↓
□ 5 □□ADC □□ ✓
   ↓
□ 6 □□Timer + PWM ✓
   ↓
□ 7 □□DMA□□□□
   ↓
□ 8-12 □□□□□□□□
   ↓
□ 13 □□RTOS□□□□
```

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- □□ STM32F446 □□□
- □□ Nucleo-F446RC □□□□
- □□ STM32F4 □□□□

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- □□ STM32CubeIDE□□□□□□□
- □□ STM32CubeMX□□□□□□□
- □□ STM32Cube Firmware□□□□□□□

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- **Windows**□ PuTTY□ Tera Term□ Arduino IDE
- **Linux**□ minicom□ picocom
- **macOS**□ minicom □ Arduino IDE

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2. □□□□ - □□□□□□□□□□
3. □□□□ - □□□□□□□□□□□□□□□
4. □□□□ - □□□□□□□□□□□□
5. □□□□ - □□□□□□□□□□

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2. □ □□□□□□□□□□
3. □ □□□□□□□□□□□□
4. □ □□ IDE □□□□□□□□□□
5. □ □□□□□□□□□□□□□

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- □□ □ `undefined reference to 'HAL_xxx'`
- □□ □ HAL □□□□□□
- □□ □ Project → Properties → C/C++ Build → Libraries □□

????

- □□ □ `Failed to connect to target`
- □□ □ ST-Link □□□□□□□□□□
- □□ □□□□□□□□□□□□□ USB

?????

- □□ □□□□ main □□□□□□□
- □□ □□□ `while(1)` □□□□□□□□□

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1. □□□□□□□□□□□□ & □□□□□□□□
2. □□ STM32 □□□□□□
3. □□□□□□□□□□□□

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□ □□□□ STM32 □□□□
□ □□□□□□□□□ UART□ SPI□ I2C□ CAN□ RS-485□
□ □□□□□□□□□□□□□
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1. □□□□ □ USB□□□□□□□□□□
2. □□□□ □□□□□□ EMC/EMI □□
3. □□□□ □□□□□□□□□□□□□□
4. □□□□ □ ARM Cortex-M0□ STM32L □□□□□

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□ □□□ **STM32 □□□□□□□□□□□□□□□□□□□□**

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□□ □□	□□□□ ___
➡ □ 1 □	□□□□ ___ + Blink LED
➡ □ 2 □	□□□□□□ ____
➡ □ 3 □	GPIO □□
➡ □ 4 □	UART □□
➡ □ 5 □	ADC □□
➡ □ 6 □	Timer + PWM
➡ □ 7 □	DMA
➡ □ 8 □	I2C
➡ □ 9 □	SPI
➡ □ 10 □	CANbus
➡ □ 11 □	RS-485
➡ □ 12 □	□□ CRC
➡ □ 13 □	RTOS

□□□□ □ 2026-03-06
□□ □ 1.0.0
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STM32 ???? ? 1 ?????? + Blink LED

STM32 ???? ? 1 ?????? + Blink LED

? ????

1. □□ **STM32CubeIDE** □□□□□ - □□□□□□□□□ IDE□□
2. □□ **Nucleo-F446RC** □□ - □□□□□□□□□□□□□□□□
3. □□□□□□□ **- Blink LED** - □□ GPIO □□ LED□□□□□□□□□□

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- **STM32 Nucleo-F446RC** □□□ × 1
- **USB Type-B** □□□ × 1□□□□□□□□
- □□□ × 1□□□□□□□□□□
- **LED + 220Ω** □□ × □□□□□□
- □□ × □□

Nucleo-F446RC ?????

Nucleo-F446RC □ STMicroelectronics □□□□□□□□□□□□ **ARM Cortex-M4** □□□ □□□□□□□□

- **User LED**□□□ **LED**□□□ **PA5**□ - □□□□□□□□□□□□
- **User Button**□□□□□□□□ **PC13**□ - □□□□□□□
- **ST-Link v2** □□□ - □□□□□□□□□□□
- □□ **GPIO** □□ - □□□□□□□□□□□

????Blink LED?

□□	□□	□□
User LED	PA5	□□□□□□□□□□
Ground	GND	□□□

> “□□ □□□□ - □□□□□ User LED □□□□□□ PA5 □□□□□□□□□□□□□□□□□□□□

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?? 1???????

□□□□□□□□ STM32CubeIDE□

1. □□□ https://www.st.com/en/development-tools/stm32cubeide.html
2. □□ **Download** □□
3. □□□□□□□□□□□ Windows / Linux / macOS□
4. □□ ST □□□□□□□□□
5. □□□□□□□□□□□ v1.13+□

?? 2??? STM32CubeIDE

Windows □□□□□

1. □□□□□ `.exe` □□□
2. □□□□□□□□□ `C:\ST\STM32CubeIDE` □
3. □□ **Add STM32CubeIDE to PATH**□□□□□□□□□□
4. □□ **Install** □□□□□□□ 5-10 □□□

Linux □□□□□

```
# □□□□□□□ tar.gz □□
tar -xzf STM32CubeIDE-*.tar.gz -C ~/opt/

# □□□□□□□□□□□□□□
cd ~/opt/STM32CubeIDE-*/
```

```
./install.sh
```

?? 3?????? Workspace ??

1. □□ STM32CubeIDE
2. □□ Workspace □□□□□□ `D:\STM32_Workspace`□
3. □□ **Launch** □□ IDE
4. □□□□□□□□□ 1-2 □□□
5. □□□□□□

?? 4???????????

1. □ USB □□□ Nucleo-F446RC □□□
2. Windows □□□□□□□□ ST-Link □□
3. □□ □□□□□ □□□
   - □□□□ **STMicroelectronics STLink** □□
   - □□□ □□□□□□□□□ https://www.st.com/en/development-tools/stsw-link009.html
4. □ STM32CubeIDE □□□□□□
   - □□ **Window → Preferences**
   - □□ **MCU → STMicroelectronics → STM32Cube**
   - □□ **ST-Link GDB server path** □□□□□□

?? CubeMX ????

?? 1??????

1. □ STM32CubeIDE □□□ **File → New → STM32 Project**
2. □□□□□□□□□ **STM32F446RC**
3. □□ **STM32F446RCTx**
4. □□ **Next →** □□□□□□□□□□ `STM32_Lesson01_BlinkLED`□
5. □□ **STM32CubeMX** □□ Toolchain
6. □□ **Finish**

?? 2?CubeMX ??

STM32CubeIDE □□□□□ CubeMX □□□□□□□□□□□□ Pinout □□

GPIO □□□

1. □ Pinout □□□□ **PA5**□□□□□ User LED□
2. □□□□□□ **GPIO_Output**□□□□□□
3. □□□□□□□□□ PA5 → □□ **GPIO_Output**
4. □□□ **System Core** □□□ **GPIO**
5. □□ **GPIOA** □□ PA5 □□□□
   - **GPIO output level**: High
   - **GPIO mode**: Output Push-Pull
   - **Pull**: No pull
   - **Maximum output speed**: High

□□□□□ **Clock Tree**□□

1. □□□ **Clock Configuration** □□
2. □□□□□□□
   - **HSE (High Speed External)**: 8 MHz□□□□□□□□□
   - **System Clock Multiplier**: □□□ **168 MHz**□ F446RC □□□□□
   - **AHB Prescaler**: 1□□□□□
3. □□□□□□□□□□

SysTick □□□

1. □□□□□ **SysTick**
2. □□ **Timebase** □□ **SysTick**
3. □□□□□□□□□□□□□□□□□

?? 3??????

1. □□ **Project → Generate Code**
2. CubeMX □□□□□□□□□□□□
3. □□ **Open Project** □□ IDE

? ?????

main.c - Blink LED ??

```
/* STM32 Lesson 01 - Blink LED
 * □□□□□ GPIO □□ PA5 (User LED)□□□ 1 □□□□□□
 * □□□□□
 */

#include "main.h"
```

```c
#include "gpio.h"

/* □□□□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);

int main(void)
{
  /* □□□□□□□□□□□□□□□ */
  HAL_Init();

  /* □□□□□□□□ 168 MHz */
  SystemClock_Config();

  /* □□□ GPIO */
  MX_GPIO_Init();

  /* □□□ */
  while (1)
  {
    /* □□ PA5 □□□□□□LED □□□*/
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_SET);

    /* □□ 500 ms */
    HAL_Delay(500);

    /* □□ PA5 □□□□□□LED □□□*/
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_RESET);

    /* □□ 500 ms */
    HAL_Delay(500);
  }
}

/**
  * @brief □□□□□□□□□
  * □□□□□□□ (HSE) □□□ 168 MHz
  */
void SystemClock_Config(void)
{
```

```
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  /** □□□□□□□□□□□□□□□ */
  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  /** □□□□ RCC □□□□ */
  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = RCC_PLLP_DIV2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }

  /** □□□□ CPU□AHB □ APB □□□□□□ */
  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□□□
  * □□ PA5 □□□□□
  */
```

```
static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  /* GPIO □□□□□ */
  __HAL_RCC_GPIOA_CLK_ENABLE();

  /* □□ GPIO □□ PA5 */
  GPIO_InitStruct.Pin = GPIO_PIN_5;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
  GPIO_InitStruct.Pull = GPIO_NOPULL;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);
}

/**
  * @brief □□□□□□
  * □□□□□□□□□□□
  */
void Error_Handler(void)
{
  while (1)
  {
    /* □□□□□□□□□□ */
  }
}
```

? ?????

????

□ □□□□□□

- □□□□□□□□ LED □ **1** □□□ □ 0.5□□ + 0.5□□□□□□□
- IDE □ Console □□ □ **Build successful**□
- □□□□□□ warnings□

?????????

□□	□□	□□□□
□□□□ □ undefined reference to HAL_xxx□	HAL □□□□□□□□	□□ CubeMX □□□□□□□□□ Project → Properties → C/C++ Build → Libraries
□□□□ □ Failed to connect to target □	ST-Link □□□□□□□□□□□□	□□□□ USB □□□□□□□□□□□ ST-Link □□
LED □□	GPIO □□□□□□□□□□	□□ CubeMX □ PA5 □□□□ GPIO_Output□□□ GPIO_PIN_SET □□□□
LED □□□□□	HAL_Delay □□□□□	□□ SysTick □□□□□ CubeMX □□□

???????

1. □□ Nucleo-F446RC □□□□ USB Type-B□
2. □ IDE □□□ **Project □□□□□□**
3. □□□□□□□ **Build □□□□□□□□□□**
4. □□□□□□□□□□□□
5. □□ **Run □□□□□□□□□** **Ctrl+F11 □□□□**
6. □□□□□□□□□ LED □□□□□□

??/??????

□□ □□□□ □

- □□ □□□□□ □ □□□ □□ PA5 □□□□□
- □□□□□ □□ □ 0V□ LED □□ ↔ 3.3V□ LED □□□□□ 1 □

? ????

?? Debug ??

1. □□□□□□□ **Debug □□□□□□□□□□□□□**
2. □□□□□□□□ `main()` □□□□□
3. □ **F6** □□□ **Step Over** □□□□
4. □□ **Variables □□□□□□□□□**
5. □ **F8** □□□ **Resume** □□□□

??????

□□□□□□□

1. □□□□□□□□□□□□□□□□□□□□□□
2. □ **Breakpoints** □□□□□□□□□□□□□□□
3. □□□□□□□□□□□□□

? ????

???? - ? 2 ????????

□ 2 □□□□□□□

- □□□□□□□□ - □□ STM32F446 □□□ 168 MHz
- **Clock Tree** □□ - □□□□□□□□□
- □□□□□□ - Sleep□ Stop□ Standby □□□□□□

????????

1. □□□□□□ - □ `HAL_Delay(500)` □□□□□□□□ LED □□□□□□
2. □□□□□□ **LED** - □□ User Button□ PC13□□□□□ LED □□
3. □□□□□ □□□□ - □□ PWM □□□□□□□□□□□□□

??????

- □□ STM32CubeIDE □□□□
- □□ STM32F446 □□□
- □□ HAL □□□□□□□
- □□ Nucleo-F446RC □□□□□

□ □□□□□□□□ **1** □□□□□□□ **STM32** □□□□

□□□□□□□ 2 □□□□□□□□□□□□□ □□

STM32 ???? ? 2 ????????

? ????

1. [illegible] **STM32F446 [illegible]** - [illegible]
2. [illegible] **Clock Tree** - [illegible] CubeMX [illegible]
3. [illegible] - [illegible] LED [illegible]

? ????????

????????

STM32 [illegible] CPU [illegible]

STM32F446 ????

STM32F446 [illegible] **3** [illegible] [illegible]

[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
HSI ([illegible])	16 MHz	±2%	[illegible]	[illegible]
HSE ([illegible])	8 MHz	±1%	[illegible]	[illegible]
LSI ([illegible])	32 kHz	±2%	[illegible]	[illegible] RTC

PLL ????

PLL (Phase-Locked Loop) [illegible]

- [illegible] 8 MHz[illegible] HSE[illegible]
- [illegible] 21
- [illegible] 8 MHz × 21 = **168 MHz**

STM32F446 [illegible] **180 MHz**[illegible] **168 MHz**[illegible]

?????

□□□□□ 168 MHz□□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□□	□□	□□□□
AHB (HCLK)	CPU □□□□□	168 MHz / 1 = 168 MHz
APB1 (PCLK1)	□□□□□ UART□ I2C□	168 MHz / 4 = 42 MHz
APB2 (PCLK2)	□□□□□ SPI□ ADC□	168 MHz / 2 = 84 MHz

?? Clock Tree ????

Clock Tree ????

```
┌─────────────────────────────────────────────┐
│  □□□□□                                      │
│  ├─ HSI (16 MHz) - □□□□□                   │
│  ├─ HSE (8 MHz)  - □□□□                     │
│  └─ LSI (32 kHz) - □□□□□□□              │
└─────────────────────────────────────────────┘
             │
             ▼
┌─────────────────────────────────────────────┐
│  PLL □□□□□□□                            │
│  □□□□□PLLM / PLLN / PLLP / PLLQ         │
│  □□□8 MHz → 168 MHz                         │
└─────────────────────────────────────────────┘
             │
             ▼
┌─────────────────────────────────────────────┐
│  □□□□□□□SYSCLK□                       │
│  ├─ HSI (16 MHz)                            │
│  ├─ HSE (8 MHz)                             │
│  └─ PLL (168 MHz)    ← □□                  │
└─────────────────────────────────────────────┘
             │
             ▼
```

```
┌──────────────────────────────────────────────┐
│  AHB □□□                                     │
│  168 MHz / 1 = 168 MHz (HCLK)                │
└──────────────┬───────────────────────────────┘
               │
     ┌─────────┼─────────┐
     ▼         ▼         ▼
  APB1      APB2      □□□□
  /4         /2
  ↓          ↓
 42MHz     84MHz
```

PLL ????

STM32F446 PLL □□□□□

```
f_VCO = (f_HSE / PLLM) × PLLN
f_PLLCLK = f_VCO / PLLP
f_USB = f_VCO / PLLQ
```

□□□□□　　HSE = 8 MHz□□

```
PLLM = 8      (□□□□8 MHz / 8 = 1 MHz)
PLLN = 168    (□□□1 MHz × 168 = 168 MHz)
PLLP = 2      (□□□□□□□168 MHz / 2 = 84 MHz) □ □□□ 84 MHz
PLLP = 2      (□□ 168 MHz□□□□ PLLN = 336, PLLP = 2)
```

□□□□□□□□□　　168 MHz□□

```
PLLM = 8      (□□□□8 MHz / 8 = 1 MHz)
PLLN = 336    (□□□1 MHz × 336 = 336 MHz)
PLLP = 2      (□□□□□□□336 MHz / 2 = 168 MHz) □
```

?? CubeMX ??????

?? 1?????????

1. □□□□□□□□□□□□□□

2. □□ `.ioc` □□□□ CubeMX
3. □□ **Clock Configuration** □□

?? 2??? HSE?8 MHz ???

1. □□ System Clock Mux□□□□□ **HSE** □□□□
2. □□ **Crystal/Ceramic Resonator**
3. □□ HSE □□□□□ **8 MHz**

?? 3??? PLL

1. □□ **PLL** □□□□
2. □□□□□□□
   - **PLLM**: 8
   - **PLLN**: 336
   - **PLLP**: 2 (□□□□□□□ 168 MHz)
   - **PLLQ**: 7 (□□ USB□□□□□□□)
3. □ PLLN □□ 336 □□□□□□□□ 168 MHz□□□ PLLP □□□ 2

?? 4??? PLL ???????

1. □□ System Clock Mux□□□□□ **PLLCLK**
2. □□ **SYSCLK** □□ **168 MHz**

?? 5??? AHB ???

1. □□ System Clock Mux□□□□□ **AHB Prescaler**
2. □□□ **1**□□□□□ HCLK = 168 MHz□

?? 6??? APB ???

1. **APB1 Prescaler** □□ **4**
   - □□□ 168 MHz / 4 = **42 MHz**□□□ UART□ I2C□
2. **APB2 Prescaler** □□ **2**
   - □□□ 168 MHz / 2 = **84 MHz**□□□ SPI□ ADC□

?? 7?????

- □ □□ CubeMX □□□□□□□□□□

```
SYSCLK:  168 MHz
HCLK:    168 MHz
PCLK1:    42 MHz
PCLK2:    84 MHz
```

? ????? - ??????

main.c - ???????

```c
/* STM32 Lesson 02 - Clock Configuration Verification
 * □□□□□□□□□□□□□□□□□□ LED □□□□□□□□□□
 * □□□□□□
 */

#include "main.h"
#include "gpio.h"

/* □□□□□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
void LED_Blink(uint16_t delay_ms, uint8_t count);

/* □□□□□□□□□□□□□□□□□□ */
uint32_t SystemCoreClock = 168000000;  /* 168 MHz */

int main(void)
{
  HAL_Init();

  /* □□□□□□□□ 168 MHz */
  SystemClock_Config();

  /* □□□ GPIO */
  MX_GPIO_Init();

  /* □□□□□□□ */
```

```
  while (1)
  {
    /* □□□□□□□□□□ (5 □□100ms □□) - □□ 168 MHz */
    LED_Blink(100, 5);
    HAL_Delay(1000);  /* □□ 1 □ */

    /* □□□□□□□□□□ (3 □□200ms □□) - □□□□□□ */
    LED_Blink(200, 3);
    HAL_Delay(1000);

    /* □□□□□□□□□□ (2 □□500ms □□) - □□□□□□ */
    LED_Blink(500, 2);
    HAL_Delay(2000);
  }
}

/**
  * @brief LED □□□□□□
  * @param delay_ms: □□□/□□□□□□□□□□
  * @param count: □□□
  */
void LED_Blink(uint16_t delay_ms, uint8_t count)
{
  for (uint8_t i = 0; i < count; i++)
  {
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_SET);    /* LED □ */
    HAL_Delay(delay_ms);
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_RESET);  /* LED □ */
    HAL_Delay(delay_ms);
  }
}

/**
  * @brief □□□□□□□□
  * □□ HSE (8 MHz) → PLL □□□ 168 MHz
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
```

```
  /* □□□□□□□□ */
  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  /* □□ RCC □□□ */
  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;      /* □□□□8 MHz / 8 = 1 MHz */
  RCC_OscInitStruct.PLL.PLLN = 336;    /* □□□1 MHz × 336 = 336 MHz */
  RCC_OscInitStruct.PLL.PLLP = 2;      /* □□□□□□336 MHz / 2 = 168 MHz */
  RCC_OscInitStruct.PLL.PLLQ = 7;      /* USB □□□336 MHz / 7 = 48 MHz */

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }

  /* □□□□□□□□ */
  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;      /* HCLK = 168 MHz */
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;       /* PCLK1 = 42 MHz */
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;       /* PCLK2 = 84 MHz */

  /* Flash □□□□□□□□□□□□□□□□□□□□□□□□ */
  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }

  /* □□ SysTick □□□□□□□ */
  HAL_SYSTICK_Config(SystemCoreClock / 1000);  /* 1ms □□□□ */
}

/**
  * @brief GPIO □□□□□
```

```
  */
static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  __HAL_RCC_GPIOA_CLK_ENABLE();

  GPIO_InitStruct.Pin = GPIO_PIN_5;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
  GPIO_InitStruct.Pull = GPIO_NOPULL;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);
}

/**
  * @brief □□□□□□□
  */
void Error_Handler(void)
{
  while (1)
  {
    HAL_GPIO_TogglePin(GPIOA, GPIO_PIN_5);  /* □□□□□□□□□□ */
    HAL_Delay(100);
  }
}
```

? ?????

????

□ **LED □□□□□□□** □

1. **5 □□□□□□** □ 100ms□ → □□□□□ 168 MHz □□
2. □□ **1** □
3. **3 □□□□□□** □ 200ms□ → □□□□□□□
4. □□ **1** □
5. **2 □□□□□□** □ 500ms□ → □□□□□□□
6. □□ **2 □□□□□**

???????

□□	□□	□□□□
LED □□□	PLL □□□□	□□ CubeMX Clock Configuration □□□ PLLN □ 336□ PLLP □ 2
□□□□□□	HAL_Delay □□□□	□□ SysTick □□□□□□□□□□ 168 MHz
□□□□ □ implicit conversion□	□□□□□□□□	□□ `SystemCoreClock` □□□□□ `uint32_t`
□□□□	Flash □□□□	□□ `FLASH_LATENCY_5` □□□□□□□□□□□□□□□

??????

□□ **1□ UART □□□□□□□□□□** □□□□□□ `SystemCoreClock` □

□□ **2□□□□□□** □□ PA5 □□□□□□□□□□□

□□ **3□□□□□**

- □ `HAL_Delay(100)` □□□□ 100ms□□□□□ □

? ????

Flash ?????FLASH_LATENCY?

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□ Latency□□

□□□□	□□ **LATENCY**
0-30 MHz	0
31-60 MHz	1
61-90 MHz	2
91-120 MHz	3
121-150 MHz	4
151-168 MHz	5

? ????

?? 1????????

□□□□□□□ **HSI (16 MHz)** □□ PLL□

```
/* □□ HSI □□ PLL□□□□□□□ */
RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
RCC_OscInitStruct.HSIState = RCC_HSI_ON;
RCC_OscInitStruct.PLL.PLLState = RCC_PLL_OFF;  /* □□ PLL */

RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_HSI;
```

□□ LED □□□□□□□□□□□□□□□□ 168 MHz □□ 16 MHz□

?? 2????????

□□□□□□□□□□□□□□□□□

? ????

???? - ? 3 ??GPIO ?????????????

□ 3 □□□□□

- **GPIO □□□□** - □□□□□□
- □□□□□ **EXTI**□ - □□□□□□
- □□□□□ - □□□□□□□□

????

- □□ STM32F446 Reference Manual - RCC□□
- □□ PLL □□□□□□□□□□

□ □□□□□□□□□□□□□ **GPIO** □□□□ □□

STM32 ???? ? 3 ??GPIO ??????????????

? ????

1. □□ **GPIO** □□□□ - □□□□□□□□□
2. □□□□□□□□□ **EXTI**□ - □□□□□□□□□□□
3. □□□□□□□□□ - □□□□□□□□□□□□□□□□□

? GPIO ????

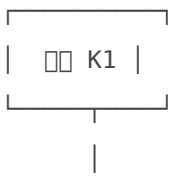
GPIO ????

GPIO□□□□□ /□□□□□□□□□□□□

□□	□□	□□
GPIO_MODE_OUTPUT_PP	□□□□□ Push-Pull□	□□ LED□□□□□□
GPIO_MODE_INPUT	□□□□	□□□□□□□□□□□
GPIO_MODE_INPUT_PU	□□□□	□□□□□□□□
GPIO_MODE_INPUT_PD	□□□□	□□□□
GPIO_MODE_AF_PP	□□□□	UART□ SPI □□□
GPIO_MODE_IT_FALLING	□□□□□□	□□□□□□□

??????

□□□□□□□□□□



```
          ┌──────┴──────┐
          │ (□□□□□□)│
          │             │
         GND          GPIO_PIN_XX (□□□□)


□□□□□
- □□□□□□GPIO = □□□ (3.3V)  → GPIO_PIN_SET
- □□□□□   GPIO = □□□ (0V)   → GPIO_PIN_RESET
```

?????Bouncing???

□□□□□□□□□□□□□□□□□□□□□□□□□□□□□ /□□□□□□□□□□□

```
□□□□□□□□□□
GPIO ┌─────┐
     └─────┘


□□□□□□□□□□
GPIO ┌──┐ ┌───┐ ┌──┐
     └──┘─┘   └─┘  └─ (□□□□)


□□□□□□□□□□□□□□□□□
```

?????

□□	□□	□□	□□
□□□□□	□□□□	□□ CPU □□	□
□□□□	□□□□	□□□ RC □□	□
□□ +□□□	□□	□□□□	□

□□□□□ □□□□□ □□□□□□

?? ????

????

□□	□□	□□
□□□□□□ Push Button□	1	□□□
220Ω □□□□	1	□□ GPIO□□□□
□□	2	□□□□□ GPIO

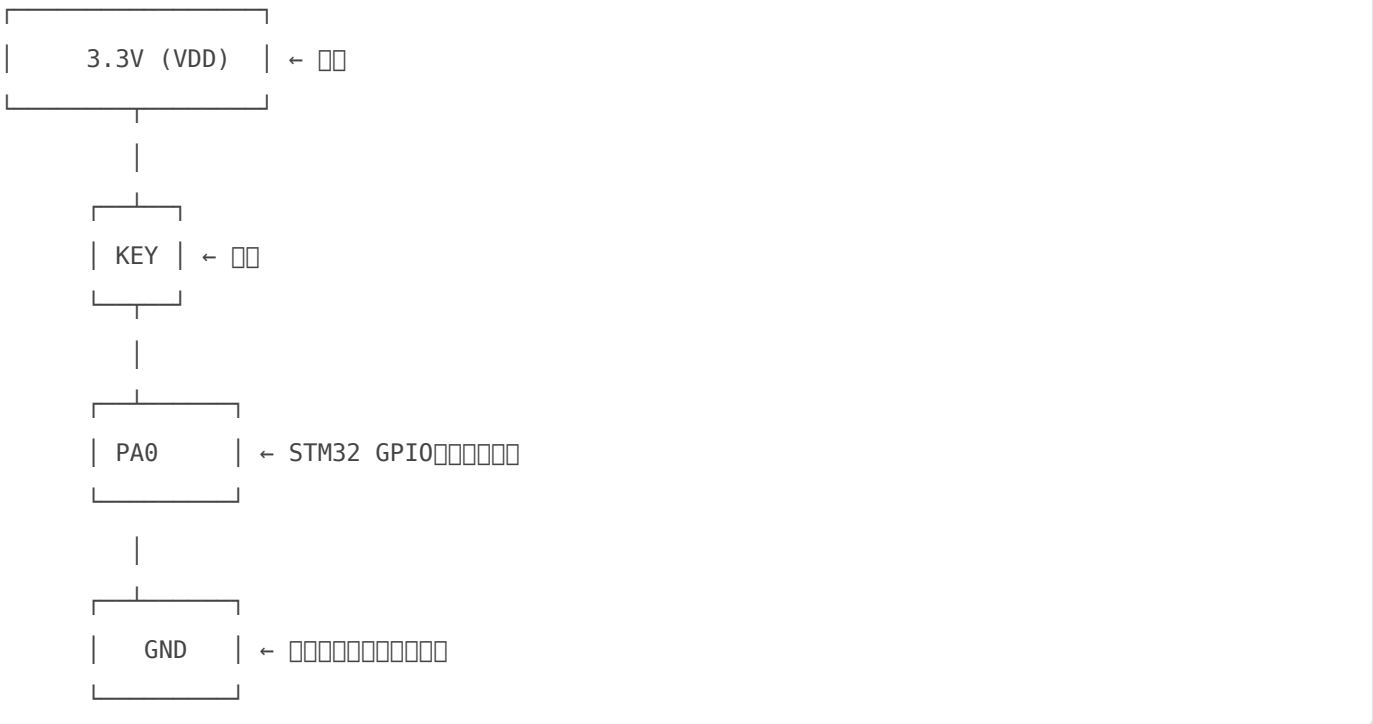
???

□□	□□	□□□□
□□ A □	3.3V	□□□□□□
□□ B □	PA0	□□□□□ GPIO□□□□
PA0	GND	□□□□□□□□
LED	PA5	□□□□□□□□□□□□

CubeMX □□ PA0□

- **GPIO mode**: Input
- **Pull**: Pull-up□□□□
- **Label**: KEY_Input

???????



?? CubeMX ????

?? 1?GPIO ????

1. □□ CubeMX□□ Pinout □□□□ **PA0**
2. □□□□ PA0□□□ **GPIO_Input**
3. □□□ **System Core □□□ GPIO**
4. □□ **GPIOA □□□** PA0 □□□□
   - **GPIO mode**: Input
   - **Pull**: Pull-up
   - **Speed**: Medium
   - **User Label**: KEY_Input

?? 2????????EXTI?

1. □□ Pinout □□□□□□ **PA0**
2. □□ **GPIO_EXTI0**□□□□□□□□
3. □□□□□ **System Core → NVIC**
4. □□ **EXTI line0 interrupt □□□ Enabled**
5. □□□□□□
   - **Preemption Priority**: 0
   - **Sub Priority**: 0

?? 3??????????? 2 ????

□□ SysTick □□□□□□□□□ HAL_Delay□

?? 4??????

□□ **Project → Generate Code**

? ?????

main.c - ???? + ???

```
/* STM32 Lesson 03 - Key Press with Debouncing
 * □□□□□□□□□□ LED□□□□□□□□□□□
 * □□□□□
```

```
 */

#include "main.h"
#include "gpio.h"

/* □□□□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
void HAL_GPIO_EXTI_Callback(uint16_t GPIO_Pin);
void Debounce_Key(void);

/* □□□□□□ */
#define DEBOUNCE_DELAY_MS  20   /* □□□□□□□□□□□□□□ */
#define DEBOUNCE_SAMPLES   3    /* □□□□□ */

/* □□□□□ */
volatile uint8_t key_pressed = 0;       /* □□□□□□□□ */
volatile uint32_t last_interrupt_time = 0;  /* □□□□□□□□ */
uint8_t led_state = 0;                  /* LED □□□0=□□1=□ */

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();

  /* □□□□□□LED □□ */
  HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_RESET);
  led_state = 0;

  while (1)
  {
    /* □□□□□□□□ */
    if (key_pressed)
    {
      Debounce_Key();
      key_pressed = 0;  /* □□□□□ */
    }

    /* □□□□□□□□□□□□□□□□□ */
```

```
  }
}

/**
  * @brief GPIO □□□□□□□□□
  * □□□□□□□□□□□□□□□□
  * @param GPIO_Pin: □□□□□□□□
  */
void HAL_GPIO_EXTI_Callback(uint16_t GPIO_Pin)
{
  if (GPIO_Pin == GPIO_PIN_0)
  {
    /* □□□□□□□□□□□□□□□□□□□□ 50ms */
    uint32_t current_time = HAL_GetTick();

    if (current_time - last_interrupt_time > 50)
    {
      key_pressed = 1;  /* □□□□□□□□□□□□□ */
      last_interrupt_time = current_time;
    }
  }
}

/**
  * @brief □□□□□□□□□
  * □□□□□□□□□□□□□
  */
void Debounce_Key(void)
{
  uint8_t stable_count = 0;

  /* □□□□□□ */
  for (uint8_t i = 0; i < DEBOUNCE_SAMPLES; i++)
  {
    /* □□□□□□ */
    GPIO_PinState key_state = HAL_GPIO_ReadPin(GPIOA, GPIO_PIN_0);

    /* □□□□□□□□□□□□□□□□□□□□ */
    if (key_state == GPIO_PIN_RESET)
    {
```

```
      stable_count++;
    }

    /* □□□□□□□ */
    HAL_Delay(DEBOUNCE_DELAY_MS);
  }

  /* □□□□□□□□□□□□□□□□□□□□□□□□□□□□□ */
  if (stable_count == DEBOUNCE_SAMPLES)
  {
    /* □□ LED □□ */
    led_state = !led_state;
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, led_state ? GPIO_PIN_SET : GPIO_PIN_RESET);
  }
}

/**
  * @brief □□□□□□□□□□ 2 □□
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }
```

```
  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□□□
  */
static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  /* GPIO □□□□□ */
  __HAL_RCC_GPIOA_CLK_ENABLE();

  /* □□ PA5□LED □□□ */
  GPIO_InitStruct.Pin = GPIO_PIN_5;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
  GPIO_InitStruct.Pull = GPIO_NOPULL;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

  /* □□ PA0□□□□□ + □□□ */
  GPIO_InitStruct.Pin = GPIO_PIN_0;
  GPIO_InitStruct.Mode = GPIO_MODE_IT_FALLING;  /* □□□□□□ */
  GPIO_InitStruct.Pull = GPIO_PULLUP;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

  /* □□ EXTI0 □□ */
  HAL_NVIC_SetPriority(EXTI0_IRQn, 0, 0);
```

```
  HAL_NVIC_EnableIRQ(EXTI0_IRQn);
}

/**
  * @brief □□□□ 0 □□□□
  */
void EXTI0_IRQHandler(void)
{
  HAL_GPIO_EXTI_IRQHandler(GPIO_PIN_0);
}

/**
  * @brief □□□□
  */
void Error_Handler(void)
{
  while (1);
}
```

? ?????

????

□ □□□□□

1. □□□□□□ **User Button□ PC13□** □□□□□
2. □□□□□□ LED □□
3. □□□□□□ LED □□
4. □□□□□□ LED □□□□

⚠ □□□□□ □ LED □□□□□□□□□□□□

???????

□□	□□	□□□□
□□□□□	□□□□□□ GPIO □□□□	□□ CubeMX □ EXTI0_IRQn □□ Enabled
LED □□□□	□□□□□□□	□□ `DEBOUNCE_DELAY_MS` □ 30-50ms

□□	□□	□□□□
□□□□□□	□□□□□□□□	□□ `key_pressed` □□□□□
□□□□ □ EXTI0_IRQHandler □□□□	□□□□□□□□ HAL □□	□□□□□□□□□□□□

????????

□□□□□□□□ Debug □□□□□□□□□□

```
/* □□□□□□□□□□□□□□□□ */
GPIO_PinState key_state = HAL_GPIO_ReadPin(GPIOA, GPIO_PIN_0);
// key_state □□ GPIO_PIN_RESET (0) = □□
//          □ GPIO_PIN_SET (1) = □□
```

? ????

???????

STM32 □□ □□□□□ □ Priority Grouping□□

- **Preemption Priority**□□□□□□□□□□□□□□□□
  - □□□ 0 > □□□ 1 > □□□ 2 ...
  - □□□□□□□□□□□□□□□□□□□
- **Sub Priority**□□□□□□□□□□□□□□□□□□□□□

```
/* □□□□□□□□ */
HAL_NVIC_SetPriority(EXTI0_IRQn, 0, 0);  /* □□=0, □=0 */
```

????????

□□□□□□□□□□□□□□□□

```
#define ANTI_BOUNCE_TIME 50  /* 50ms □□□□ */

void HAL_GPIO_EXTI_Callback(uint16_t GPIO_Pin)
{
  static uint32_t last_time = 0;
  uint32_t now = HAL_GetTick();
```

```
  /* □□□□□□□□ > 50ms□□□□□□□□□ */
  if (now - last_time > ANTI_BOUNCE_TIME)
  {
    key_pressed = 1;
    last_time = now;
  }
}
```

? ????

?? 1?????

□□□□□□□□□□□□□□□□ UART □□□□□□□□□□ UART □□□□

?? 2?????

□□□□□□□□□□ 2 □□□□□□□□□

```
void Handle_Long_Press(void)
{
  uint32_t press_time = HAL_GetTick();

  while (HAL_GPIO_ReadPin(GPIOA, GPIO_PIN_0) == GPIO_PIN_RESET)
  {
    if (HAL_GetTick() - press_time > 2000)  /* 2 □ */
    {
      /* □□□□ */
      return;
    }
  }
}
```

?? 3??????

□□□□□□□□□□□□□□□□□□□□□□

? ????

???? - ? 4 ??UART ???? + ????

□ 4 □□□□□

- **UART □□□□□□**
- □□□□□□□□□
- □□□□□□□□□□

????

- □□ STM32F446 GPIO and EXTI Reference
- □□ □□□□□□□□□□□□□□

□ GPIO □□□□□□□□□□□□□ - UART □□

STM32 ???? ? 4 ??UART ???? + ????

? ????

1. □□ **UART** □□□□ - □□□□□□□□□□□□
2. □□□□□□ - □□□□□ STM32 □□□□
3. □□□□□□ - □□□□□□□□□□□

? UART ????

UART ????

UART (Universal Asynchronous Receiver/Transmitter) □□□□□□□□□□□□□□□

- □□□□ □□□□□□□□□□□□□□
- □□□ □□□□□□
- □□□ □□□□□□□□□□

UART ???

STM32F446 UART □□□□ 3 □□□

□□	□□	□□
TX (USART1_TX)	□□	PA9
RX (USART1_RX)	□□	PA10
GND	□□□	GND

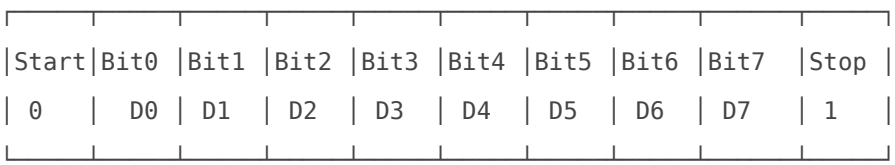
????????

□□	□□□	□□
Baud Rate	9600 / 115200	□□□□□□□

□□	□□□	□□
Data Bits	8	□□□□□□ 8 □□□
Stop Bits	1	□□□
Parity	None	□□□□□

UART ?????

1 □ UART □□□□□□□



?? ????

????

□□	□□	□□
USB-UART □□□	1	PL2303 □ CP2102 □□
USB Type-A □□□	1	□□□□
□□□	3	□□ STM32 □□□□

???

STM32	USB-UART	□□
PA9 (TX)	RX	STM32 □□□□□
PA10 (RX)	TX	STM32 □□□□□□
GND	GND	□□□□

> ⚠ □□ - □□□□□□ ST-Link □□□□□□□ USB □□ UART □□□□□□□□□□□

?? CubeMX ????

?? 1??? USART1

1. □□ CubeMX
2. □ Pinout □□□□ PA9 □ PA10
3. □□□□□□
   - PA9□ **USART1_TX**
   - PA10□ **USART1_RX**
4. □□□□□ Connectivity → **USART1** □□□□

?? 2??? USART1 ??

1. □□□□□ **Connectivity → USART1**
2. □□□□□□□
   - **Baud Rate**: 115200
   - **Word Length**: 8 Bits
   - **Stop Bits**: 1
   - **Parity**: None
   - **Mode**: Asynchronous (RX and TX)

?? 3??? USART1 ??

1. □□ **NVIC Settings** □□
2. □□ **USART1 global interrupt**
3. □□□□□
   - **Preemption Priority**: 1
   - **Sub Priority**: 0

?? 4??????

□□ **Generate Code**

? ?????

main.c - UART + ????

```
/* STM32 Lesson 04 - UART Communication with Command Interface
 * □□□□□□ UART □□□□□□□□□ LED
 * □□□
 *   "ON"  - LED □□
 *   "OFF" - LED □□
 *   "TOGGLE" - LED □□
 * □□□□□□
 */

#include "main.h"
#include "usart.h"
#include "gpio.h"
#include <string.h>
#include <stdio.h>

/* □□□□□ */
#define UART_RX_BUFFER_SIZE 50
#define COMMAND_MAX_LEN 20

/* □□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);
void Process_Command(char *command);
void HAL_UART_RxCpltCallback(UART_HandleTypeDef *huart);

/* □□□□□ */
UART_HandleTypeDef huart1;
uint8_t uart_rx_buffer[UART_RX_BUFFER_SIZE];
uint8_t uart_rx_index = 0;
char command_buffer[COMMAND_MAX_LEN];

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_USART1_UART_Init();
```

```
  /* □□□ LED □□□□□ */
  HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_RESET);

  /* □□□□□□ */
  UART_Print("\r\n==============================\r\n");
  UART_Print("STM32 UART Command Interface\r\n");
  UART_Print("Commands: ON, OFF, TOGGLE\r\n");
  UART_Print("==============================\r\n");

  /* □□ UART □□□□ */
  HAL_UART_Receive_IT(&huart1, uart_rx_buffer, 1);

  while (1)
  {
    /* □□□ */
  }
}

/**
  * @brief UART □□□□□printf □□□
  */
void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

/**
  * @brief UART □□□□□□□□
  */
void HAL_UART_RxCpltCallback(UART_HandleTypeDef *huart)
{
  if (huart->Instance == USART1)
  {
    uint8_t received_char = uart_rx_buffer[0];
```

```
    /* □□□□□□□\r□□□□□□□\n□ */
    if (received_char == '\r' || received_char == '\n')
    {
      if (uart_rx_index > 0)
      {
        command_buffer[uart_rx_index] = '\0';
        UART_Print("\r\nReceived: %s\r\n", command_buffer);
        Process_Command(command_buffer);
        uart_rx_index = 0;
      }
    }
    else if (received_char == 0x08 || received_char == 0x7F)  /* Backspace */
    {
      if (uart_rx_index > 0)
      {
        uart_rx_index--;
        UART_Print("\b \b");  /* □□□□ */
      }
    }
    else
    {
      /* □□□□ */
      if (uart_rx_index < COMMAND_MAX_LEN - 1)
      {
        command_buffer[uart_rx_index++] = received_char;
        HAL_UART_Transmit(&huart1, (uint8_t *)&received_char, 1, HAL_MAX_DELAY);  /* Echo */
      }
    }

    /* □□□□□□□□□□ */
    HAL_UART_Receive_IT(&huart1, uart_rx_buffer, 1);
  }
}

/**
  * @brief □□□□□□
  */
void Process_Command(char *command)
{
```

```
  /* □□□□□□□□□□ */
  for (int i = 0; command[i]; i++)
  {
    if (command[i] >= 'a' && command[i] <= 'z')
      command[i] -= 32;
  }

  /* □□□□ */
  if (strcmp(command, "ON") == 0)
  {
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_SET);
    UART_Print("LED ON\r\n");
  }
  else if (strcmp(command, "OFF") == 0)
  {
    HAL_GPIO_WritePin(GPIOA, GPIO_PIN_5, GPIO_PIN_RESET);
    UART_Print("LED OFF\r\n");
  }
  else if (strcmp(command, "TOGGLE") == 0)
  {
    HAL_GPIO_TogglePin(GPIOA, GPIO_PIN_5);
    UART_Print("LED TOGGLED\r\n");
  }
  else
  {
    UART_Print("Unknown command. Try: ON, OFF, TOGGLE\r\n");
  }

  UART_Print("> ");  /* □□□□□ */
}

/**
  * @brief □□□□□□
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
```

```
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□
  */
static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  __HAL_RCC_GPIOA_CLK_ENABLE();

  GPIO_InitStruct.Pin = GPIO_PIN_5;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
```

```
  GPIO_InitStruct.Pull = GPIO_NOPULL;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);
}

/**
  * @brief USART1 □□□
  */
static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;
  huart1.Init.HwFlowCtl = UART_HWCONTROL_NONE;
  huart1.Init.OverSampling = UART_OVERSAMPLING_16;

  if (HAL_UART_Init(&huart1) != HAL_OK)
  {
    Error_Handler();
  }

  __HAL_UART_ENABLE_IT(&huart1, UART_IT_RXNE);
}

/**
  * @brief UART MSP □□□
  */
void HAL_UART_MspInit(UART_HandleTypeDef* huart)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  if(huart->Instance == USART1)
  {
    __HAL_RCC_USART1_CLK_ENABLE();
    __HAL_RCC_GPIOA_CLK_ENABLE();

    GPIO_InitStruct.Pin = GPIO_PIN_9 | GPIO_PIN_10;
```

```
    GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
    GPIO_InitStruct.Pull = GPIO_NOPULL;
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_VERY_HIGH;
    GPIO_InitStruct.Alternate = GPIO_AF7_USART1;
    HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

    HAL_NVIC_SetPriority(USART1_IRQn, 1, 0);
    HAL_NVIC_EnableIRQ(USART1_IRQn);
  }
}

/**
  * @brief USART1 □□□□
  */
void USART1_IRQHandler(void)
{
  HAL_UART_IRQHandler(&huart1);
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

□ □□□□□

1. □□□□□□□
2. □□□□□□□□ PuTTY□ Arduino IDE □ Tera Term□
3. □□ COM □□□□□ 115200
4. □□□□□□□□ `STM32 UART Command Interface`
5. □□ `ON` → LED □□□□□ `LED ON`
6. □□ `OFF` → LED □□□□□ `LED OFF`
7. □□ `TOGGLE` → LED □□□□

????

□□	□□	□□□□
□□□□□	UART □□□□□□□□□	□□ CubeMX □ USART1 □□□□□□□□ 115200
□□	□□□□□	□□□□□□□□□□□□ 9600 □ 115200
□□□□□□	□□□□□	□□ `HAL_UART_Receive_IT()` □□□
□□□□	□□□□□□□	□□□□ `ON` □ `OFF` □ `TOGGLE` □□□□□□□□□

? ????

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□□□□□□□□□□□□□□□□□□□

DMA + UART

□□ DMA □□□□□□ UART □□□□□□□

? ????

???? - ? 5 ??ADC???????????? channel?

□ 5 □□□□□

- **ADC** □□□□□□□□
- □□□□□□□□□
- □□□□□

□ **UART** □□□□□□□□

STM32 ???? ? 5 ??ADC ???????????? Channel?

? ????

1. □□ **ADC** □□□□ - □□□□□□□□□□
2. □□□□□□□□□□ - □□□□□□□□□□
3. □□□□□□□□ - □□ UART □□□□□□□□

? ADC ????

ADC ????

ADC (Analog-to-Digital Converter) □□□□□□□□□□□□□□□□ STM32F446 □□ **3** □ **ADC**□□ **16** □□□□ □

STM32F446 ADC ??

□□	□□
□□□	12 □□ 0-4095□
□□□□	~3.5 □□
□□□	□□ 2.4 MSPS□□□□ /□□
□□□	19 □□ 16 □□ + 3 □□□
□□□□	3.3V□ VREF+□

ADC ????

□□	**GPIO** □□	□□
ADC1_IN0	PA0	□□□□□□□ 1
ADC1_IN1	PA1	□□□□□□□ 2

□□	**GPIO □□**	□□
ADC1_IN2	PA2	□□□□□□□ 3
ADC1_IN3	PA3	□□□□□□□ 4
ADC1_IN16	-	□□□□□□□□□
ADC1_IN17	-	VREF+ □□□□
ADC1_IN18	-	VBAT□□□□□□

??????????

```
□□□□ (V)          □□□ (12-bit)
     ↑                   ↑
3.3V ├──────────────────  4095
2.5V ├──────────────────  3077 (□ 2.5V/3.3V * 4095)
1.65V├──────────────────  2048
0.0V ├──────────────────  0
     └──────────────────  └────────
```

□□□□□

```
ADC_Value = (V_input / V_ref) × 2^Resolution - 1
          = (V_input / 3.3) × 4095

□□□
V_input = (ADC_Value / 4095) × 3.3
```

ADC ????

□□	□□	□□
Single	□□□□□□□□	□□□□
Continuous	□□□□□□	□□□□
Scan	□□□□□□□□	□□□□
DMA	□□□□□□□	□□□□□□

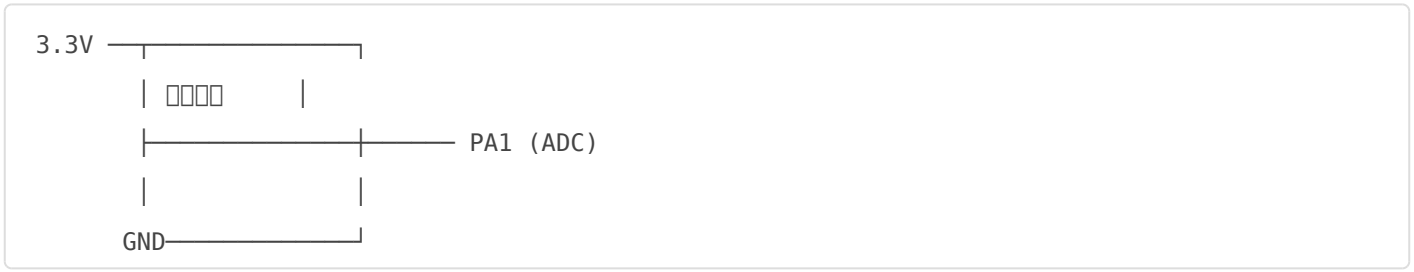
?? ????

????

□□	□□	□□
□□□□□ 10kΩ□	1	□□□□□□□□
□□□	3	□□□
□□	□□	□□□□□

???

□□	□□	□□
□□□□□□□	PA1 (ADC1_IN1)	□□□□
□□□□□□	3.3V	□□
□□□□□□□	GND	□□



?? CubeMX ????

?? 1??? ADC1

1. □□ CubeMX
2. □□ **Analog → ADC1**
3. □ Pinout □□□□ **PA1** □□□ **ADC1_IN1**

?? 2??? ADC ??

1. □□□□□ **Analog → ADC1**
2. □ **Parameter Settings** □□□□
   - **Resolution**: 12 Bits
   - **Data Alignment**: Right Alignment
   - **Scan Conversion Mode**: Disable□□□□□
   - **Continuous Conversion Mode**: Enable□□□□□□

- **EOC Selection**: End of conversion flag

3. □ **Rank □□□□□□**
   - □□ **Add □ Rank 1**
   - □□ **Channel**: ADC_CHANNEL_1
   - **Sampling Time**: 144 Cycles□□□□

?? 3??? ADC ??

1. □□ **NVIC Settings □□**
2. □□ **ADC1 global interrupt**
3. □□□□□
   - **Preemption Priority**: 2
   - **Sub Priority**: 0

?? 4?????????????

□□□□□□□□□ Timer □□ ADC□

1. □□ **Timers → TIM2**
2. □□ **Trigger Output Event**: Update Event
3. □□ ADC1□□□□
   - **Trigger**: Timer2 Trigger Out Event
   - **External Trigger Conversion Edge**: Rising Edge

?? 5??????

□□ **Generate Code**

? ?????

main.c - ADC ??????????

```
/* STM32 Lesson 05 - ADC Continuous Sampling
 * □□□□□□□□□□□□□□□□ UART □□□□
 * □□□□□
 */

#include "main.h"
#include "adc.h"
```

```c
#include "usart.h"
#include "gpio.h"
#include <stdio.h>
#include <stdarg.h>

/* □□□□□ */
#define ADC_SAMPLE_SIZE 10  /* □□□□□□□□□ */

/* □□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_ADC1_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);
uint16_t ADC_Get_Average(void);
float ADC_To_Voltage(uint16_t adc_value);

/* □□□□□ */
ADC_HandleTypeDef hadc1;
UART_HandleTypeDef huart1;
volatile uint16_t adc_value = 0;
volatile uint8_t adc_conversion_complete = 0;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_USART1_UART_Init();
  MX_ADC1_Init();

  UART_Print("\r\n======= ADC Sampling Test =======\r\n");
  UART_Print("ADC Value | Voltage (V)\r\n");
  UART_Print("==================================\r\n");

  /* □□ ADC □□□□ */
  HAL_ADC_Start_IT(&hadc1);

  while (1)
  {
```

```
    if (adc_conversion_complete)
    {
      /* □□ ADC □ */
      uint16_t adc_raw = HAL_ADC_GetValue(&hadc1);

      /* □□□□□ */
      float voltage = ADC_To_Voltage(adc_raw);

      /* □□ UART □□ */
      UART_Print("%-9d | %.2f\r\n", adc_raw, voltage);

      adc_conversion_complete = 0;

      HAL_Delay(100);  /* □ 100ms □□□□ */
    }
  }
}

/**
  * @brief □ ADC □□□□□□□□
  * @param adc_value: 12-bit ADC □□□ (0-4095)
  * @return □□□□V□
  */
float ADC_To_Voltage(uint16_t adc_value)
{
  /* □□□□ 3.3V□12-bit □□□ 4095 */
  return (adc_value / 4095.0f) * 3.3f;
}

/**
  * @brief □□□□□□□□□□□
  */
uint16_t ADC_Get_Average(void)
{
  uint32_t sum = 0;

  for (uint8_t i = 0; i < ADC_SAMPLE_SIZE; i++)
  {
    sum += HAL_ADC_GetValue(&hadc1);
    HAL_Delay(1);
```

```
  }

  return sum / ADC_SAMPLE_SIZE;
}

/**
  * @brief ADC □□□□□□□□□
  */
void HAL_ADC_ConvCpltCallback(ADC_HandleTypeDef* hadc)
{
  if (hadc->Instance == ADC1)
  {
    adc_conversion_complete = 1;
  }
}

/**
  * @brief UART □□□□
  */
void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

/**
  * @brief □□□□□□
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
```

```
  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□
  */
static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  __HAL_RCC_GPIOA_CLK_ENABLE();

  GPIO_InitStruct.Pin = GPIO_PIN_5;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
  GPIO_InitStruct.Pull = GPIO_NOPULL;
```

```
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);
}

/**
  * @brief ADC1 □□□
  */
static void MX_ADC1_Init(void)
{
  ADC_ChannelConfTypeDef sConfig = {0};

  hadc1.Instance = ADC1;
  hadc1.Init.ClockPrescaler = ADC_CLOCK_SYNC_PCLK_DIV4;
  hadc1.Init.Resolution = ADC_RESOLUTION_12B;
  hadc1.Init.ScanConvMode = DISABLE;
  hadc1.Init.ContinuousConvMode = ENABLE;
  hadc1.Init.DiscontinuousConvMode = DISABLE;
  hadc1.Init.ExternalTrigConv = ADC_SOFTWARE_START;
  hadc1.Init.DataAlign = ADC_DATAALIGN_RIGHT;
  hadc1.Init.NbrOfConversion = 1;
  hadc1.Init.DMAContinuousRequests = DISABLE;
  hadc1.Init.EOCSelection = ADC_EOC_SINGLE_CONV;
  hadc1.Init.LowPowerAutoWait = DISABLE;
  hadc1.Init.LowPowerAutoPowerOff = DISABLE;

  if (HAL_ADC_Init(&hadc1) != HAL_OK)
  {
    Error_Handler();
  }

  sConfig.Channel = ADC_CHANNEL_1;
  sConfig.Rank = 1;
  sConfig.SamplingTime = ADC_SAMPLETIME_144CYCLES;

  if (HAL_ADC_ConfigChannel(&hadc1, &sConfig) != HAL_OK)
  {
    Error_Handler();
  }
}
```

```
/**
  * @brief ADC MSP □□□
  */
void HAL_ADC_MspInit(ADC_HandleTypeDef* hadc)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  if(hadc->Instance == ADC1)
  {
    __HAL_RCC_ADC1_CLK_ENABLE();
    __HAL_RCC_GPIOA_CLK_ENABLE();

    GPIO_InitStruct.Pin = GPIO_PIN_1;
    GPIO_InitStruct.Mode = GPIO_MODE_ANALOG;
    GPIO_InitStruct.Pull = GPIO_NOPULL;
    HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

    HAL_NVIC_SetPriority(ADC_IRQn, 2, 0);
    HAL_NVIC_EnableIRQ(ADC_IRQn);
  }
}

/**
  * @brief USART1 □□□
  */
static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;
  huart1.Init.HwFlowCtl = UART_HWCONTROL_NONE;
  huart1.Init.OverSampling = UART_OVERSAMPLING_16;

  if (HAL_UART_Init(&huart1) != HAL_OK)
  {
    Error_Handler();
  }
```

```
}

/**
  * @brief UART MSP □□□
  */
void HAL_UART_MspInit(UART_HandleTypeDef* huart)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  if(huart->Instance == USART1)
  {
    __HAL_RCC_USART1_CLK_ENABLE();
    __HAL_RCC_GPIOA_CLK_ENABLE();

    GPIO_InitStruct.Pin = GPIO_PIN_9 | GPIO_PIN_10;
    GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
    GPIO_InitStruct.Pull = GPIO_NOPULL;
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_VERY_HIGH;
    GPIO_InitStruct.Alternate = GPIO_AF7_USART1;
    HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

    HAL_NVIC_SetPriority(USART1_IRQn, 1, 0);
    HAL_NVIC_EnableIRQ(USART1_IRQn);
  }
}

/**
  * @brief ADC □□□□
  */
void ADC_IRQHandler(void)
{
  HAL_ADC_IRQHandler(&hadc1);
}

/**
  * @brief USART1 □□□□
  */
void USART1_IRQHandler(void)
{
  HAL_UART_IRQHandler(&huart1);
```

```
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

□ ADC □□□□□

```
======= ADC Sampling Test =======
ADC Value | Voltage (V)
==================================
2048      | 1.65
3072      | 2.47
1024      | 0.82
4095      | 3.30
```

□□□□□□□ ADC □□□□□□□□□□□□□ 0.0V ~ 3.3V

????

□□	□□	□□□□
ADC □□□	□□□□□□□	□□ `HAL_ADC_Start_IT()` □□□
□□□□□	□□□□□□□□□□□	□□ `SamplingTime` □ 144 Cycles
□□□□□□	□□□□□□□□	□□□□□ V_ref □ 3.3V
□ UART □□	UART □□□□	□□ USART1 □□□□□

? ????

???????

```
/* CubeMX □□ */
hadc1.Init.ScanConvMode = ENABLE;  /* □□□□□□ */
hadc1.Init.NbrOfConversion = 3;    /* 3 □□□ */

/* □□□□□□ */
sConfig.Channel = ADC_CHANNEL_0;
sConfig.Rank = 1;
HAL_ADC_ConfigChannel(&hadc1, &sConfig);

sConfig.Channel = ADC_CHANNEL_1;
sConfig.Rank = 2;
HAL_ADC_ConfigChannel(&hadc1, &sConfig);

sConfig.Channel = ADC_CHANNEL_2;
sConfig.Rank = 3;
HAL_ADC_ConfigChannel(&hadc1, &sConfig);
```

?? DMA ??????

□□□□□□ 7 □ DMA□□□□□□□□ DMA □□□□□□ ADC □□

? ????

???? - ? 6 ????? Timer + PWM

□ 6 □□□□□

- □□□□□□□□□□
- **PWM** □□□□
- □□□□□□□

□ **ADC** □□□□□□□□□

STM32 ???? ? 6 ????? Timer + PWM

? ????

1. □□□□□□□□□□ - □□□□□□□□□□
2. □□ **PWM** □□□□ - □□ LED □□□□
3. □□□□□□□□ - □□□□□□□□□□□□

? ?????

???????

□□□□□□□□□□□□□□□□□ STM32F446 □□ **14** □□□□ □□□□□

- □□□□□□□
- PWM □□□□
- □□□□□□
- □□□□□□

STM32F446 ?????

□□□	□□	□□	□□
□□□□□ (TIM1, TIM8)	2	16-bit	PWM□□□□□□□□□
□□□□□ (TIM2-5)	4	16/32-bit	PWM□□□□□
□□□□□ (TIM6-7)	2	16-bit	□□□□ DMA □□
□□□□□ (TIM9-14)	6	16-bit	□□□□

□□□□ **TIM2**□□□□□□ □□□□

PWM ????

PWM (Pulse Width Modulation) □□□□□□□□□□□□□□□□□□□□

□□□ 25%□

```
┌──┐          ┌──┐          ┌──┐
│  │──────────│  │──────────│  │
└──┘          └──┘          └──┘
```

□□□ 50%□

```
┌─────┐      ┌─────┐      ┌─────┐
│     │──────│     │──────│     │
└─────┘      └─────┘      └─────┘
```

□□□ 75%□

```
┌─────────┐  ┌─────────┐  ┌─────────┐
│         │──│         │──│         │
└─────────┘  └─────────┘  └─────────┘
```

□□ (T) □□□□□□□□ (W) □□

□□□ (Duty Cycle) = W / T × 100%

PWM ??

□□	□□
LED □□□□	□□□□□□□□□□ 0%-100%□
□□□□□□	□□□□□□□□□
□□□□□□	□□□□□□□□
□□□□	□□□□□□

?? ????

????

□□	□□
LED	1
□□□□ (220Ω)	1
□□	2

???

□□	□□	□□
LED □□	PA15 (TIM2_CH1)	PWM □□
LED □□	GND	□□
□□□□	LED □□□□	□□ LED

?? CubeMX ????

?? 1??? TIM2 PWM

1. □□ CubeMX
2. □□ **Timers → TIM2**
3. □ Pinout □□□□ **PA15 □□□** **TIM2_CH1**

?? 2??? TIM2 ??

1. □□□□□ **Timers → TIM2**
2. **Clock Source**: Internal Clock
3. **Channel1**: PWM Generation CH1
4. **Configuration □□□□**
   - **Prescaler**: 839□□□□
   - **Counter Period**: 99□□□□□□□ ARR□
   - **Pulse**: 50□□□□□□ 50%□

□□□□□

```
PWM Frequency = SystemClock / ((Prescaler + 1) × ARR)
              = 168MHz / ((839 + 1) × 100)
              = 168MHz / 84000
              ≈ 2 kHz
```

?? 3????????

1. □□ **NVIC Settings □□**
2. □□ **TIM2 global interrupt□□□□□□□□**
3. □□□□□□ Preemption Priority = 3

?? 4??????

□□ **Generate Code**

? ?????

main.c - PWM ?????

```
/* STM32 Lesson 06 - PWM Breathing LED
 * □□□□□□ PWM □□ LED □□□□□□
 * □□□□□□
 */

#include "main.h"
#include "tim.h"
#include "gpio.h"

/* □□□□□ */
#define PWM_MAX_VALUE    99    /* PWM □□□ (ARR) */
#define BREATHE_SPEED    10    /* □□□□□ */

/* □□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_TIM2_Init(void);
void Breathing_LED(void);
void Set_PWM(uint16_t pulse_value);

/* □□□□□ */
TIM_HandleTypeDef htim2;
volatile uint16_t pwm_value = PWM_MAX_VALUE / 2;  /* □□ 50% □□□ */
volatile int8_t breathe_direction = 1;             /* 1: □□, -1: □□ */

int main(void)
{
  HAL_Init();
  SystemClock_Config();
```

```
  MX_GPIO_Init();
  MX_TIM2_Init();

  /* □□ TIM2 PWM */
  HAL_TIM_PWM_Start(&htim2, TIM_CHANNEL_1);
  HAL_TIM_Base_Start_IT(&htim2);

  while (1)
  {
    Breathing_LED();
  }
}

/**
  * @brief □□□□□
  * □□□□ PWM □□□
  */
void Breathing_LED(void)
{
  static uint8_t counter = 0;

  counter++;

  /* □ BREATHE_SPEED □□□□□□□ PWM */
  if (counter >= BREATHE_SPEED)
  {
    counter = 0;

    /* □□ PWM □ */
    pwm_value += breathe_direction;

    /* □□□□□□□ */
    if (pwm_value <= 0)
    {
      pwm_value = 0;
      breathe_direction = 1;  /* □□□□ */
    }
    else if (pwm_value >= PWM_MAX_VALUE)
    {
      pwm_value = PWM_MAX_VALUE;
```

```
      breathe_direction = -1;  /* □□□□ */
    }

    /* □□ PWM */
    Set_PWM(pwm_value);
  }
}

/**
  * @brief □□ PWM □□□
  * @param pulse_value: □□□□ (0 ~ PWM_MAX_VALUE)
  */
void Set_PWM(uint16_t pulse_value)
{
  __HAL_TIM_SET_COMPARE(&htim2, TIM_CHANNEL_1, pulse_value);
}

/**
  * @brief □□□□□□□
  */
void HAL_TIM_PeriodElapsedCallback(TIM_HandleTypeDef *htim)
{
  if (htim->Instance == TIM2)
  {
    /* □□□□□□□□□□□ */
  }
}

/**
  * @brief □□□□□□
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
```

```
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□
  */
static void MX_GPIO_Init(void)
{
  __HAL_RCC_GPIOA_CLK_ENABLE();
}

/**
  * @brief TIM2 □□□
  */
static void MX_TIM2_Init(void)
{
  TIM_ClockConfigTypeDef sClockSourceConfig = {0};
```

```
TIM_MasterConfigTypeDef sMasterConfig = {0};
TIM_OC_InitTypeDef sConfigOC = {0};

htim2.Instance = TIM2;
htim2.Init.Prescaler = 839;
htim2.Init.CounterMode = TIM_COUNTERMODE_UP;
htim2.Init.Period = 99;
htim2.Init.ClockDivision = TIM_CLOCKDIVISION_DIV1;
htim2.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE;

if (HAL_TIM_Base_Init(&htim2) != HAL_OK)
{
  Error_Handler();
}

sClockSourceConfig.ClockSource = TIM_CLOCKSOURCE_INTERNAL;
if (HAL_TIM_ConfigClockSource(&htim2, &sClockSourceConfig) != HAL_OK)
{
  Error_Handler();
}

if (HAL_TIM_PWM_Init(&htim2) != HAL_OK)
{
  Error_Handler();
}

sMasterConfig.MasterOutputTrigger = TIM_TRGO_RESET;
sMasterConfig.MasterSlaveMode = TIM_MASTERSLAVEMODE_DISABLE;
if (HAL_TIMEx_MasterConfigSynchronization(&htim2, &sMasterConfig) != HAL_OK)
{
  Error_Handler();
}

sConfigOC.OCMode = TIM_OCMODE_PWM1;
sConfigOC.Pulse = 50;
sConfigOC.OCPolarity = TIM_OCPOLARITY_HIGH;
sConfigOC.OCFastMode = TIM_OCFAST_DISABLE;

if (HAL_TIM_PWM_ConfigChannel(&htim2, &sConfigOC, TIM_CHANNEL_1) != HAL_OK)
{
```

```
    Error_Handler();
  }

  HAL_TIM_MspPostInit(&htim2);
}

/**
  * @brief TIM2 MSP □□□
  */
void HAL_TIM_MspInit(TIM_HandleTypeDef* htim)
{
  if(htim->Instance == TIM2)
  {
    __HAL_RCC_TIM2_CLK_ENABLE();
    HAL_NVIC_SetPriority(TIM2_IRQn, 3, 0);
    HAL_NVIC_EnableIRQ(TIM2_IRQn);
  }
}

/**
  * @brief TIM2 MSP □□□□□GPIO □□□
  */
void HAL_TIM_MspPostInit(TIM_HandleTypeDef* htim)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  if(htim->Instance == TIM2)
  {
    __HAL_RCC_GPIOA_CLK_ENABLE();

    GPIO_InitStruct.Pin = GPIO_PIN_15;
    GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
    GPIO_InitStruct.Pull = GPIO_NOPULL;
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
    GPIO_InitStruct.Alternate = GPIO_AF1_TIM2;
    HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);
  }
}

/**
```

```
  * @brief TIM2 □□□□
  */
void TIM2_IRQHandler(void)
{
  HAL_TIM_IRQHandler(&htim2);
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

□ **LED** □□□□□□ □

- LED □□□□□□□□□□□□
- □□□□□□□□□□
- □□□□

????

□□	□□	□□□□
LED □□	GPIO □□□ AF □□□□	□□ `HAL_TIM_MspPostInit()` □ Alternate □□
PWM □□□□□□□	Prescaler □ ARR □□□□	□□□□□□□□
□□□□□□□	BREATHE_SPEED □□□	□□□□□□□□□

? ????

???? - ? 7 ??DMA?????????

□ 7 □□□□□

- **DMA □□□□□□□**
- **ADC + DMA □□□□**
- **UART + DMA □□□□**

□ □□□□ **PWM □□□□□□□**

STM32 ???? ? 7 ??DMA ??????????

? ????

1. □□ **DMA** □□□□ - □□ CPU □□□□□□□□□
2. □□ **ADC + DMA** - □□□□□□
3. □□ **UART + DMA** - □□□□□□

? DMA ????

DMA ????

DMA (Direct Memory Access) □□□□□□□□□□□□□□□□□□□□□□□□ CPU □□□□□□

- □□□□ □ CPU □□□□□□□
- □□□□ □□□□□□□
- □□□□ □ CPU □□□□□

STM32F446 DMA ??

□□	□□
DMA □□□	2 □□ DMA1□ DMA2□
□□□	□ 16 □□□ Streams□
□□	□□ 8 □□□□ Channels□
□□□□□□	168 MB/s

DMA ????

□□	□□	□□
Normal	□□□□□□□□	□□□□□□□
Circular	□□□□□□□□□□	□□□□ /□□

□□	□□	□□
Mem-to-Mem	□□□□□□□	□□□□

?? ADC + DMA ??

?? 1?? CubeMX ??? DMA

1. □□□□□ ADC □□
2. □□ **Analog → ADC1**
3. □ **DMA Settings** □□□ **Add**
4. □□□
   - **DMA Controller**: DMA2
   - **Stream**: Stream 0 (□□□□□□)
   - **Channel**: Channel 0 (ADC1)
   - **Priority**: High

?? 2??? DMA ??

1. □□□□□ **DMA1** □ **DMA2**
2. □□□□ Stream □□□□□
   - **Mode**: Circular□□□□□□□□□□□□□
   - **Increment Address**: Enable (Memory)
   - **Data Width**: Word (32-bit)

?? 3????????

? ?????

main.c - ADC + DMA ????

```
/* STM32 Lesson 07 - ADC with DMA Circular Mode
 * □□□□□ DMA □□□□□□ ADC □□
 * □□□□□-□□
 */
```

```c
#include "main.h"
#include "adc.h"
#include "dma.h"
#include "usart.h"
#include "gpio.h"
#include <stdio.h>
#include <string.h>

/* □□ */
#define ADC_BUFFER_SIZE 100

/* □□□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_ADC1_Init(void);
static void MX_DMA_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);

/* □□□□□ */
ADC_HandleTypeDef hadc1;
DMA_HandleTypeDef hdma_adc1;
UART_HandleTypeDef huart1;

/* ADC □□□□□□□DMA □□□□□□□ */
uint16_t adc_buffer[ADC_BUFFER_SIZE];
volatile uint8_t adc_half_complete = 0;
volatile uint8_t adc_complete = 0;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_DMA_Init();
  MX_ADC1_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== ADC + DMA Test ===\r\n");
```

```
  /* □□ ADC DMA □□ */
  HAL_ADC_Start_DMA(&hadc1, (uint32_t *)adc_buffer, ADC_BUFFER_SIZE);

  uint32_t sample_count = 0;

  while (1)
  {
    if (adc_complete)
    {
      adc_complete = 0;

      /* □□□□□ */
      uint32_t sum = 0;
      for (uint16_t i = 0; i < ADC_BUFFER_SIZE; i++)
      {
        sum += adc_buffer[i];
      }
      uint16_t average = sum / ADC_BUFFER_SIZE;
      float voltage = (average / 4095.0f) * 3.3f;

      sample_count++;
      UART_Print("Sample %ld: ADC=%d, V=%.2f\r\n", sample_count, average, voltage);

      HAL_Delay(500);
    }
  }
}

/**
  * @brief ADC DMA □□□□□□
  */
void HAL_ADC_ConvCpltCallback(ADC_HandleTypeDef* hadc)
{
  if (hadc->Instance == ADC1)
  {
    adc_complete = 1;
  }
}

/**
```

```
  * @brief ADC DMA □□□□□
  */
void HAL_ADC_ConvHalfCpltCallback(ADC_HandleTypeDef* hadc)
{
  if (hadc->Instance == ADC1)
  {
    adc_half_complete = 1;
  }
}

/**
  * @brief UART □□□□
  */
void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

/**
  * @brief □□□□□□
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
```

```
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
  {
    Error_Handler();
  }

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□
  */
static void MX_GPIO_Init(void)
{
  __HAL_RCC_GPIOA_CLK_ENABLE();
}

/**
  * @brief DMA □□□
  */
static void MX_DMA_Init(void)
{
  __HAL_RCC_DMA2_CLK_ENABLE();

  HAL_NVIC_SetPriority(DMA2_Stream0_IRQn, 0, 0);
  HAL_NVIC_EnableIRQ(DMA2_Stream0_IRQn);
}
```

```
/**
  * @brief ADC1 □□□□DMA □□□
  */
static void MX_ADC1_Init(void)
{
  ADC_ChannelConfTypeDef sConfig = {0};

  hadc1.Instance = ADC1;
  hadc1.Init.ClockPrescaler = ADC_CLOCK_SYNC_PCLK_DIV4;
  hadc1.Init.Resolution = ADC_RESOLUTION_12B;
  hadc1.Init.ScanConvMode = DISABLE;
  hadc1.Init.ContinuousConvMode = ENABLE;
  hadc1.Init.DiscontinuousConvMode = DISABLE;
  hadc1.Init.ExternalTrigConv = ADC_SOFTWARE_START;
  hadc1.Init.DataAlign = ADC_DATAALIGN_RIGHT;
  hadc1.Init.NbrOfConversion = 1;
  hadc1.Init.DMAContinuousRequests = ENABLE;  /* □□ DMA □□□□ */
  hadc1.Init.EOCSelection = ADC_EOC_SINGLE_CONV;

  if (HAL_ADC_Init(&hadc1) != HAL_OK)
  {
    Error_Handler();
  }

  sConfig.Channel = ADC_CHANNEL_1;
  sConfig.Rank = 1;
  sConfig.SamplingTime = ADC_SAMPLETIME_144CYCLES;

  if (HAL_ADC_ConfigChannel(&hadc1, &sConfig) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief USART1 □□□
  */
static void MX_USART1_UART_Init(void)
{
```

```
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
  {
    Error_Handler();
  }
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

□ □□□□ □ DMA □□□□□□□□ CPU □□□□

? ????

???? - ? 8 ??I2C ?? + DMA

□ 8 □□□□□

- **I2C** □□□□□
- □□□□□□□
- **I2C + DMA** □□□□

□ **DMA** □□□□□□□

STM32 ???? ? 8 ??I2C ?? + DMA

? ????

1. □□ **I2C** □□ - □□□□□□□□□□
2. **I2C** □□□□□□ - □□□□□□□□
3. **I2C + DMA** - □□□□□□□□

? I2C ??

I2C ??

- □□□□□□ □□□□□□ (SCL) □□□□ (SDA)
- □□□□□□□□ □□□□□□□
- □□□□ □□□□□□□□□
- □□ □□□ 100 kHz□□□ 400 kHz

I2C ???

□□	**GPIO**	□□
SCL	PB6	□□□□□□□□□
SDA	PB7	□□□□□□□□□

I2C ??

I2C □□ 7 □□ 10 □□□□□□□□□□□□□□□□□□□□

- **MPU6050**□ 0x68□□□□□ +□□□□
- **BMP280**□ 0x77□□□□□
- **EEPROM**□ 0x50~0x57

?? ????

□□	□□	□□
SCL	PB6	I2C1_SCL□ 4.7kΩ □□□ 3.3V□
SDA	PB7	I2C1_SDA□ 4.7kΩ □□□ 3.3V□
VCC	3.3V	□□
GND	GND	□□

?? CubeMX ??

?? 1??? I2C1

1. **Connectivity → I2C1**
2. □□□□ **I2C**

?? 2??? I2C ??

- **Speed Mode**□ Fast (400 kHz)
- **Addressing Mode**□ 7-bit

?? 3?????

NVIC Settings □□□ I2C1 event interrupt □ I2C1 error interrupt

---

? ?????

```
/* STM32 Lesson 08 - I2C Communication */

#include "main.h"
#include "i2c.h"
#include "usart.h"

I2C_HandleTypeDef hi2c1;
UART_HandleTypeDef huart1;
```

```
/* I2C □□□□ */
HAL_StatusTypeDef I2C_Write(uint8_t addr, uint8_t reg, uint8_t data)
{
  uint8_t buffer[2] = {reg, data};
  return HAL_I2C_Master_Transmit(&hi2c1, addr << 1, buffer, 2, 1000);
}

/* I2C □□□□ */
HAL_StatusTypeDef I2C_Read(uint8_t addr, uint8_t reg, uint8_t *data, uint16_t size)
{
  HAL_I2C_Master_Transmit(&hi2c1, addr << 1, &reg, 1, 1000);
  return HAL_I2C_Master_Receive(&hi2c1, addr << 1, data, size, 1000);
}

void UART_Print(const char *format, ...);

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_I2C1_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== I2C Test ===\r\n");

  /* □□ I2C □□□□ */
  for (uint8_t addr = 0x08; addr < 0x78; addr++)
  {
    if (HAL_I2C_IsDeviceReady(&hi2c1, addr << 1, 1, 100) == HAL_OK)
    {
      UART_Print("Device found at 0x%X\r\n", addr);
    }
  }

  while (1)
  {
    /* I2C □□□□ */
  }
}
```

- **I2C □□□□□□□**

STM32 ???? ? 9 ??SPI ?? + DMA

? ????

1. □□ **SPI** □□□□□□ - □□□□□□□□□□□
2. □□ **SPI** □□□□□ - □□□□□□□□□□□□□□□
3. □□ **SPI + DMA** - □□□□□□□□

? SPI ????

SPI ????

SPI (Serial Peripheral Interface) □□□□□□□□□□□□□□□

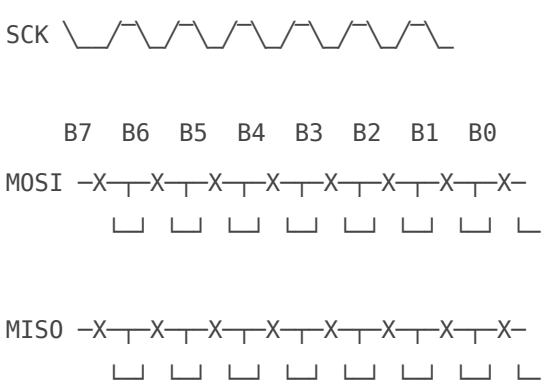
- □□□ □□□ 54 MHz□ STM32F446□
- □□□□ □□□□□□□□□
- □□□ □□□□□□□
- □□□□ □□□□□

SPI ? 4 ????

□□	□□	□□	□□
MOSI	Master Out Slave In	□ → □	□□□□□□□
MISO	Master In Slave Out	□ → □	□□□□□□□
SCK	Serial Clock	□ → □	□□□□□□□□
CS/NSS	Chip Select	□ → □	□□□□□□□□□

SPI ???

```
CS  ╲_____________________________╱
```



STM32F446 SPI ??

□□	□□
SPI □□	3 □□ SPI1□ SPI2□ SPI3□
□□□□	54 MHz□ SPI1□□ 27 MHz□ SPI2/3□
DMA □□	□□□□□□□□□
□□	SPI□ I²S□ Simplex□□□□

?? ????

????

□□	□□	□□
SPI □□□□ MPU6050□ SD□□□	1	□□
□□	4	MOSI□ MISO□ SCK□ CS

???

Nucleo □□	**SPI1**	□□	□□□□□
PA5	SCK	□□	SCL □ SCK
PA6	MISO	□□	MISO □ DO
PA7	MOSI	□□	MOSI □ DIN
PA4	CS	□□	CS □ CE

???????

```
Nucleo F446RC          □□□□□ MPU6050□
──────────────         ──────────────────

PA5 (SCK) ──────────── SCL
PA6 (MISO) ─────────── SDA/MISO
PA7 (MOSI) ─────────── MOSI
PA4 (CS)  ──────────── CS
3.3V ──────────────── VCC
GND ──────────────── GND
```

?? CubeMX ????

?? 1??? SPI1

1. □□ CubeMX
2. □ Pinout □□□□□□□□□
   - **PA5**□□□□ **SPI1_SCK**
   - **PA6**□□□□ **SPI1_MISO**
   - **PA7**□□□□ **SPI1_MOSI**
   - **PA4**□□□□ **GPIO_Output**□□□ CS □□□

?? 2??? SPI ??

1. □□□□□ **Connectivity → SPI1**
2. **Mode**□ Full-Duplex Master
3. **Configuration** □□□□
   - **Frame Format**□ Motorola□□□□□□
   - **Data Size**□ 8 Bits
   - **Prescaler**□ 8□□□ SPI □□ = 168MHz / 8 = 21 MHz□
   - **CPOL (Clock Polarity)**□ Low
   - **CPHA (Clock Phase)**□ 1 Edge□□□□□□□□□
   - **NSS (Chip Select Mode)**□ Software□□□□□ CS□

?? 3??? DMA????????

1. □ **DMA Settings** □□
   - **Add** → □□ Tx DMA□ DMA2 Stream3 Channel3
   - **Add** → □□ Rx DMA□ DMA2 Stream2 Channel3

2. □□□□□
   - **Mode**□ Normal
   - **Increment Address**□ Enable (Memory)
   - **Data Width**□ Byte

?? 4?????

NVIC Settings □□□□

- **SPI1 global interrupt**
- **DMA2 Stream2 global interrupt**□ Rx□
- **DMA2 Stream3 global interrupt**□ Tx□

?? 5??????

□□ **Generate Code**

? ?????

main.c - SPI DMA ??

```
/* STM32 Lesson 09 - SPI with DMA
 * □□□□□□ SPI + DMA □□□□□□□
 * □□□□□□
 */

#include "main.h"
#include "spi.h"
#include "dma.h"
#include "usart.h"
#include "gpio.h"
#include <stdio.h>
#include <string.h>

/* □□ */
#define SPI_TX_BUFFER_SIZE 256
#define SPI_RX_BUFFER_SIZE 256
#define CS_PORT GPIOA
```

```
#define CS_PIN GPIO_PIN_4

/* □□□□ */
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_SPI1_Init(void);
static void MX_DMA_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);
void SPI_CS_Enable(void);
void SPI_CS_Disable(void);
void SPI_Transmit_DMA(uint8_t *tx_data, uint16_t size);
void SPI_Receive_DMA(uint8_t *rx_data, uint16_t size);

/* □□□□ */
SPI_HandleTypeDef hspi1;
DMA_HandleTypeDef hdma_spi1_rx;
DMA_HandleTypeDef hdma_spi1_tx;
UART_HandleTypeDef huart1;

uint8_t spi_tx_buffer[SPI_TX_BUFFER_SIZE];
uint8_t spi_rx_buffer[SPI_RX_BUFFER_SIZE];

volatile uint8_t spi_tx_complete = 0;
volatile uint8_t spi_rx_complete = 0;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_DMA_Init();
  MX_SPI1_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== SPI DMA Test ===\r\n");

  /* □□□□□□ */
  for (uint16_t i = 0; i < 8; i++)
  {
```

```
    spi_tx_buffer[i] = 0xA0 + i;  /* □□□□□□ */
  }

  while (1)
  {
    UART_Print("Sending via SPI DMA...\r\n");

    /* □□ DMA □□ */
    SPI_Transmit_DMA(spi_tx_buffer, 8);

    /* □□□□ */
    while (!spi_tx_complete);
    spi_tx_complete = 0;

    UART_Print("Sent: ");
    for (uint16_t i = 0; i < 8; i++)
    {
      UART_Print("0x%02X ", spi_tx_buffer[i]);
    }
    UART_Print("\r\n");

    HAL_Delay(1000);
  }
}

/**
  * @brief SPI □□□□□□□□
  */
void SPI_CS_Enable(void)
{
  HAL_GPIO_WritePin(CS_PORT, CS_PIN, GPIO_PIN_RESET);
  HAL_Delay(1);  /* □□□□□□ */
}

/**
  * @brief SPI □□□□□□□□
  */
void SPI_CS_Disable(void)
{
  HAL_Delay(1);  /* □□□□□□□□ */
```

```
  HAL_GPIO_WritePin(CS_PORT, CS_PIN, GPIO_PIN_SET);
}

/**
  * @brief □□ DMA □□□□
  */
void SPI_Transmit_DMA(uint8_t *tx_data, uint16_t size)
{
  SPI_CS_Enable();
  HAL_SPI_Transmit_DMA(&hspi1, tx_data, size);
}

/**
  * @brief □□ DMA □□□□
  */
void SPI_Receive_DMA(uint8_t *rx_data, uint16_t size)
{
  SPI_CS_Enable();
  HAL_SPI_Receive_DMA(&hspi1, rx_data, size);
}

/**
  * @brief SPI □□□□□□□□
  */
void HAL_SPI_TxCpltCallback(SPI_HandleTypeDef *hspi)
{
  if (hspi->Instance == SPI1)
  {
    spi_tx_complete = 1;
    SPI_CS_Disable();
  }
}

/**
  * @brief SPI □□□□□□□□
  */
void HAL_SPI_RxCpltCallback(SPI_HandleTypeDef *hspi)
{
  if (hspi->Instance == SPI1)
  {
```

```
    spi_rx_complete = 1;
    SPI_CS_Disable();
  }
}

/**
  * @brief UART □□□□
  */
void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

/**
  * @brief □□□□□□
  */
void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
```

```
  {
    Error_Handler();
  }

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief GPIO □□□
  */
static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  __HAL_RCC_GPIOA_CLK_ENABLE();

  /* □□ PA4 □ CS □□ */
  GPIO_InitStruct.Pin = GPIO_PIN_4;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
  GPIO_InitStruct.Pull = GPIO_NOPULL;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

  /* □□□ CS □□□□□□□□□ */
  HAL_GPIO_WritePin(GPIOA, GPIO_PIN_4, GPIO_PIN_SET);
}

/**
  * @brief SPI1 □□□
  */
```

```
static void MX_SPI1_Init(void)
{
  hspi1.Instance = SPI1;
  hspi1.Init.Mode = SPI_MODE_MASTER;
  hspi1.Init.Direction = SPI_DIRECTION_2LINES;
  hspi1.Init.DataSize = SPI_DATASIZE_8BIT;
  hspi1.Init.CLKPolarity = SPI_POLARITY_LOW;
  hspi1.Init.CLKPhase = SPI_PHASE_1EDGE;
  hspi1.Init.NSS = SPI_NSS_SOFT;
  hspi1.Init.BaudRatePrescaler = SPI_BAUDRATEPRESCALER_8;
  hspi1.Init.FirstBit = SPI_FIRSTBIT_MSB;
  hspi1.Init.TIMode = SPI_TIMODE_DISABLE;
  hspi1.Init.CRCCalculation = SPI_CRCCALCULATION_DISABLE;
  hspi1.Init.CRCPolynomial = 10;

  if (HAL_SPI_Init(&hspi1) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief DMA □□□
  */
static void MX_DMA_Init(void)
{
  __HAL_RCC_DMA2_CLK_ENABLE();

  HAL_NVIC_SetPriority(DMA2_Stream2_IRQn, 1, 0);
  HAL_NVIC_EnableIRQ(DMA2_Stream2_IRQn);

  HAL_NVIC_SetPriority(DMA2_Stream3_IRQn, 1, 0);
  HAL_NVIC_EnableIRQ(DMA2_Stream3_IRQn);
}

/**
  * @brief USART1 □□□
  */
static void MX_USART1_UART_Init(void)
{
```

```
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
  {
    Error_Handler();
  }
}

/**
  * @brief SPI1 MSP □□□
  */
void HAL_SPI_MspInit(SPI_HandleTypeDef* hspi)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};

  if(hspi->Instance == SPI1)
  {
    __HAL_RCC_SPI1_CLK_ENABLE();
    __HAL_RCC_GPIOA_CLK_ENABLE();

    GPIO_InitStruct.Pin = GPIO_PIN_5 | GPIO_PIN_6 | GPIO_PIN_7;
    GPIO_InitStruct.Mode = GPIO_MODE_AF_PP;
    GPIO_InitStruct.Pull = GPIO_NOPULL;
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_VERY_HIGH;
    GPIO_InitStruct.Alternate = GPIO_AF5_SPI1;
    HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

    hdma_spi1_rx.Instance = DMA2_Stream2;
    hdma_spi1_rx.Init.Channel = DMA_CHANNEL_3;
    hdma_spi1_rx.Init.Direction = DMA_PERIPH_TO_MEMORY;
    hdma_spi1_rx.Init.PeriphInc = DMA_PINC_DISABLE;
    hdma_spi1_rx.Init.MemInc = DMA_MINC_ENABLE;
    hdma_spi1_rx.Init.PeriphDataAlignment = DMA_PDATAALIGN_BYTE;
    hdma_spi1_rx.Init.MemDataAlignment = DMA_MDATAALIGN_BYTE;
    hdma_spi1_rx.Init.Mode = DMA_NORMAL;
```

```
    hdma_spi1_rx.Init.Priority = DMA_PRIORITY_HIGH;
    HAL_DMA_Init(&hdma_spi1_rx);

    __HAL_LINKDMA(hspi, hdmarx, hdma_spi1_rx);

    hdma_spi1_tx.Instance = DMA2_Stream3;
    hdma_spi1_tx.Init.Channel = DMA_CHANNEL_3;
    hdma_spi1_tx.Init.Direction = DMA_MEMORY_TO_PERIPH;
    hdma_spi1_tx.Init.PeriphInc = DMA_PINC_DISABLE;
    hdma_spi1_tx.Init.MemInc = DMA_MINC_ENABLE;
    hdma_spi1_tx.Init.PeriphDataAlignment = DMA_PDATAALIGN_BYTE;
    hdma_spi1_tx.Init.MemDataAlignment = DMA_MDATAALIGN_BYTE;
    hdma_spi1_tx.Init.Mode = DMA_NORMAL;
    hdma_spi1_tx.Init.Priority = DMA_PRIORITY_HIGH;
    HAL_DMA_Init(&hdma_spi1_tx);

    __HAL_LINKDMA(hspi, hdmatx, hdma_spi1_tx);

    HAL_NVIC_SetPriority(SPI1_IRQn, 1, 0);
    HAL_NVIC_EnableIRQ(SPI1_IRQn);
  }
}

/**
  * @brief SPI1 □□□□
  */
void SPI1_IRQHandler(void)
{
  HAL_SPI_IRQHandler(&hspi1);
}

/**
  * @brief DMA2 Stream2 □□□□
  */
void DMA2_Stream2_IRQHandler(void)
{
  HAL_DMA_IRQHandler(&hdma_spi1_rx);
}

/**
```

```
  * @brief DMA2 Stream3 □□□□
  */
void DMA2_Stream3_IRQHandler(void)
{
  HAL_DMA_IRQHandler(&hdma_spi1_tx);
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

□ □□□□ □

- UART □□ "Sending via SPI DMA..."
- □□□□□□□□□□□□ 0xA0 ~ 0xA7□
- LED □□□□□□□□□□

????

□□	□□	□□□□
□ **UART** □□	UART □□□□	□□ MX_USART1_UART_Init()
SPI □□□	DMA □□□□	□□ DMA Stream □ Channel □□
□□□□□	Prescaler □□	□□ Prescaler □□□□□
□□□□	CPOL/CPHA □□□	□□□□□□□□□

? ????

SPI ? SD ???

```
/* SD □□□□ */
#define SD_CS_PORT GPIOA
#define SD_CS_PIN GPIO_PIN_4

void SD_Init(void)
{
  SPI_CS_Enable();
  /* □□□□□□□ CMD0 */
  uint8_t cmd[6] = {0x40, 0x00, 0x00, 0x00, 0x00, 0x95};
  HAL_SPI_Transmit(&hspi1, cmd, 6, 100);
  SPI_CS_Disable();
}
```

? ????

???? - ? 10 ??CANbus ?????????

□ 10 □□□□□

- **CAN □□□□□□**
- **TJA1050 □□□□□**
- **Nucleo ↔ Nucleo □□□□**

□ **SPI □□□□□□□□□**

STM32 ???? ? 10 ??CANbus ??????????

? ????

1. □□ **CAN** □□□□ - □□□□□□□□□□□□□□□□□
2. □□ **TJA1050** □□□ - □□ Nucleo □ Nucleo □ CAN □□
3. □□□□□□□□ - □□□□□□□□□□□□□□□

? CAN ????

CAN ????

CAN (Controller Area Network) □□□□□□□□□□□□□□□

- □□□□ □□□□□□□
- □□□□ □□□□□□□□□□□□□□
- □□□ □□ 127 □□□□□□□□□
- □□□ □□ 40km□□□□□ 1km□□□□

CAN ????

□□	□□	□□
ID	11 bit□□□□ / 29 bit□□□□	□□□□□□□□□□□□□
DLC	4 bit	□□□□□□ 0-8 bytes□
DATA	0-8 bytes	□□□□□□
CRC	15 bit	□□□□□□□□□□□□□

CAN ? 2 ????

□□	□□
CAN_H	CAN □□□

□□	□□
CAN_L	CAN □□□
GND	□□□□

STM32F446 CAN ??

□□	□□
CAN □□	2 □□ CAN1□ CAN2□
□□□□	1 Mbps
□□□□□	14 □□□□□
□□ **FIFO**	2 □□□□ 3 □□□
□□□□	3 □

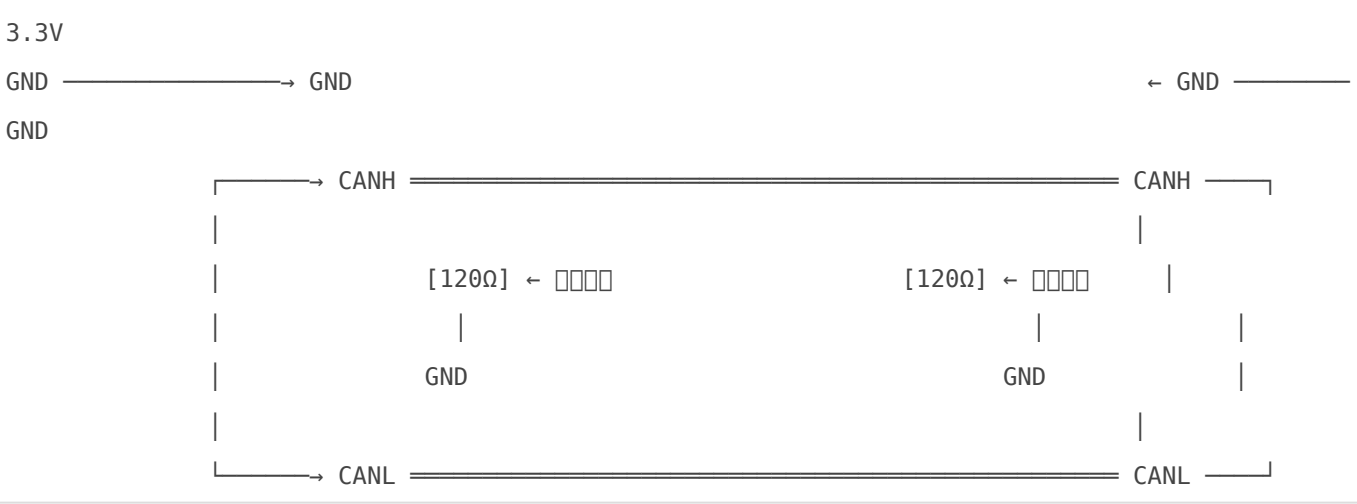
?? ????

????

□□	□□	□□
Nucleo-F446RC	2	□□□□
TJA1050 CAN □□□	2	□□ CAN □□
120Ω □□□□	2	CAN □□□□□ 1
□□	□□	□□□□□□

CAN ????

```
Nucleo 1 (□□□)      TJA1050 □□□      CAN □□      TJA1050 □□□      Nucleo 2 (□□□)
──────────────────      ──────────────────    ──────────    ──────────────────
──────────────────
PB8 (CAN1_RX) ──────→ RXD                                          ← RXD ─── PB8
(CAN1_RX)
PB9 (CAN1_TX) ←────── TXD                                          ──→ TXD ─── PB9
(CAN1_TX)
3.3V ──────────────→ VCC                                           ← VCC ──────
```



???

| Nucleo PB8 | → | TJA1050 RXD | | Nucleo PB9 | ← | TJA1050 TXD | | Nucleo 3.3V | → | TJA1050 VCC | | Nucleo GND | → | TJA1050 GND | | TJA1050 CANH | = | CAN □□ H | | TJA1050 CANL | = | CAN □□ L |

?? CubeMX ????

?? 1??? CAN1???????????

1. □□ CubeMX
2. □ Pinout □□□□□
   - **PB8□□□□ CAN1_RX**
   - **PB9□□□□ CAN1_TX**

?? 2??? CAN ??

1. □□□□□ **Connectivity → CAN1**
2. **Activated□□□□□**
3. **Parameter Settings** □□□□
   - **Mode□** Normal Mode
   - **Prescaler□** 8□□□□ = 168MHz / 8 / 13 ≈ 1.615 Mbps□□□□ 1 Mbps□
   - **Time Quanta in Bit Segment 1□** 11
   - **Time Quanta in Bit Segment 2□** 2
   - **Resynchronization Jump Width□** 1
4. **Filter Settings□□□□□□**
   - **Number of Master Filters□** 14
   - **Filters Configuration□**

- **Filter ID**： 0x000□□□□□ ID□
- **Filter Mask**： 0x000
- **Filter FIFO Assignment**： FIFO0
- **Filter Activation**： Enable

?? 3?????

NVIC Settings □□□□

- **CAN1 RX0 interrupt**
- **CAN1 TX interrupt**

?? 4??????

□□ **Generate Code**

? ?????

??? (Nucleo 1)?main.c

```
/* STM32 Lesson 10 - CAN Bus (Sender)
 * □□□□□ CAN □□□□□□ Nucleo
 * □□□□□
 */

#include "main.h"
#include "can.h"
#include "usart.h"
#include <stdio.h>
#include <string.h>

#define CAN_ID_TEST 0x123  /* CAN □□ ID */

void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_CAN1_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);
```

```
CAN_HandleTypeDef hcan1;
UART_HandleTypeDef huart1;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_CAN1_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== CAN Bus Sender ===\r\n");

  CAN_TxHeaderTypeDef TxHeader;
  uint8_t TxData[8];
  uint32_t TxMailbox;

  /* □□□□□□□□ */
  TxHeader.StdId = CAN_ID_TEST;
  TxHeader.IDE = CAN_ID_STD;     /* □□ ID */
  TxHeader.RTR = CAN_RTR_DATA;   /* □□□□□□□□□□ */
  TxHeader.DLC = 8;              /* □□□□ 8 bytes */

  uint32_t counter = 0;

  while (1)
  {
    /* □□□□□□ */
    TxData[0] = (counter >> 24) & 0xFF;
    TxData[1] = (counter >> 16) & 0xFF;
    TxData[2] = (counter >> 8) & 0xFF;
    TxData[3] = counter & 0xFF;
    TxData[4] = 0xAA;
    TxData[5] = 0xBB;
    TxData[6] = 0xCC;
    TxData[7] = 0xDD;

    /* □□□□ */
    if (HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox) == HAL_OK)
```

```
    {
      UART_Print("CAN Sent: ID=0x%03X, Data=[%02X %02X %02X %02X %02X %02X %02X %02X]\r\n",
        CAN_ID_TEST,
        TxData[0], TxData[1], TxData[2], TxData[3],
        TxData[4], TxData[5], TxData[6], TxData[7]);
      counter++;
    }
    else
    {
      UART_Print("CAN Send Failed!\r\n");
    }

    HAL_Delay(1000);
  }
}

void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
```

```
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    Error_Handler();

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
    Error_Handler();
}

static void MX_GPIO_Init(void)
{
  __HAL_RCC_GPIOA_CLK_ENABLE();
}

static void MX_CAN1_Init(void)
{
  hcan1.Instance = CAN1;
  hcan1.Init.Prescaler = 6;
  hcan1.Init.Mode = CAN_MODE_NORMAL;
  hcan1.Init.SyncJumpWidth = CAN_SJW_1TQ;
  hcan1.Init.TimeSeg1 = CAN_BS1_11TQ;
  hcan1.Init.TimeSeg2 = CAN_BS2_2TQ;
  hcan1.Init.TimeTriggeredMode = DISABLE;
  hcan1.Init.AutoBusOff = DISABLE;
  hcan1.Init.AutoWakeUp = DISABLE;
  hcan1.Init.AutoRetransmission = ENABLE;
  hcan1.Init.ReceiveFifoLocked = DISABLE;
  hcan1.Init.TransmitFifoPriority = DISABLE;

  if (HAL_CAN_Init(&hcan1) != HAL_OK)
    Error_Handler();
```

```
  HAL_CAN_Start(&hcan1);
}

static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
    Error_Handler();
}

void Error_Handler(void)
{
  while(1);
}
```

??? (Nucleo 2)?main.c

```
/* STM32 Lesson 10 - CAN Bus (Receiver)
 * □□□□□□□□□ Nucleo □ CAN □□
 * □□□□□□
 */

#include "main.h"
#include "can.h"
#include "usart.h"
#include <stdio.h>
#include <string.h>

#define CAN_ID_TEST 0x123

void SystemClock_Config(void);
static void MX_GPIO_Init(void);
```

```
static void MX_CAN1_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);

CAN_HandleTypeDef hcan1;
UART_HandleTypeDef huart1;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_CAN1_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== CAN Bus Receiver ===\r\n");

  CAN_FilterTypeDef sFilterConfig;
  CAN_RxHeaderTypeDef RxHeader;
  uint8_t RxData[8];

  /* □□□□□□□□ */
  sFilterConfig.FilterBank = 0;
  sFilterConfig.FilterMode = CAN_FILTERMODE_IDMASK;
  sFilterConfig.FilterScale = CAN_FILTERSCALE_32BIT;
  sFilterConfig.FilterIdHigh = (CAN_ID_TEST << 5) >> 16;
  sFilterConfig.FilterIdLow = (CAN_ID_TEST << 5) & 0xFFFF;
  sFilterConfig.FilterMaskIdHigh = 0xFFFF;
  sFilterConfig.FilterMaskIdLow = 0xFFFF;
  sFilterConfig.FilterFIFOAssignment = CAN_RX_FIFO0;
  sFilterConfig.FilterActivation = ENABLE;

  if (HAL_CAN_ConfigFilter(&hcan1, &sFilterConfig) != HAL_OK)
    Error_Handler();

  if (HAL_CAN_Start(&hcan1) != HAL_OK)
    Error_Handler();

  /* □□□□□□ */
  if (HAL_CAN_ActivateNotification(&hcan1, CAN_IT_RX_FIFO0_MSG_PENDING) != HAL_OK)
```

```
    Error_Handler();

  UART_Print("Waiting for CAN messages...\r\n");

  while (1)
  {
    if (HAL_CAN_GetRxFifoFillLevel(&hcan1, CAN_RX_FIFO0) > 0)
    {
      if (HAL_CAN_GetRxMessage(&hcan1, CAN_RX_FIFO0, &RxHeader, RxData) == HAL_OK)
      {
        UART_Print("CAN Received: ID=0x%03X, DLC=%d, Data=[%02X %02X %02X %02X %02X %02X %02X
%02X]\r\n",
          RxHeader.StdId, RxHeader.DLC,
          RxData[0], RxData[1], RxData[2], RxData[3],
          RxData[4], RxData[5], RxData[6], RxData[7]);
      }
    }
    HAL_Delay(100);
  }
}

void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
```

```
  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    Error_Handler();

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
    Error_Handler();
}

static void MX_GPIO_Init(void)
{
  __HAL_RCC_GPIOA_CLK_ENABLE();
}

static void MX_CAN1_Init(void)
{
  hcan1.Instance = CAN1;
  hcan1.Init.Prescaler = 6;
  hcan1.Init.Mode = CAN_MODE_NORMAL;
  hcan1.Init.SyncJumpWidth = CAN_SJW_1TQ;
  hcan1.Init.TimeSeg1 = CAN_BS1_11TQ;
  hcan1.Init.TimeSeg2 = CAN_BS2_2TQ;
  hcan1.Init.TimeTriggeredMode = DISABLE;
  hcan1.Init.AutoBusOff = DISABLE;
  hcan1.Init.AutoWakeUp = DISABLE;
  hcan1.Init.AutoRetransmission = ENABLE;
```

```
  hcan1.Init.ReceiveFifoLocked = DISABLE;
  hcan1.Init.TransmitFifoPriority = DISABLE;

  if (HAL_CAN_Init(&hcan1) != HAL_OK)
    Error_Handler();
}

static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
    Error_Handler();
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

UART

```
=== CAN Bus Sender ===
CAN Sent: ID=0x123, Data=[00 00 00 00 AA BB CC DD]
CAN Sent: ID=0x123, Data=[00 00 00 01 AA BB CC DD]
CAN Sent: ID=0x123, Data=[00 00 00 02 AA BB CC DD]
```

UART

```
=== CAN Bus Receiver ===
Waiting for CAN messages...
CAN Received: ID=0x123, DLC=8, Data=[00 00 00 00 AA BB CC DD]
CAN Received: ID=0x123, DLC=8, Data=[00 00 00 01 AA BB CC DD]
CAN Received: ID=0x123, DLC=8, Data=[00 00 00 02 AA BB CC DD]
```

????

□□	□□	□□
□□□□	□□□□□□□	□□□□ Prescaler/TimeSeg
□□□□	CAN □□□□□□□□	□□□□□□ 120Ω □□
□□□□	□□□	□□ HAL_CAN_GetTxMailboxesFreeLevel()

? ????

???? - ? 11 ??RS-485 ??

□ 11 □□□□□

- **RS-485** □□□□
- □□□□□□□□
- □□□□□□□□

□ **CAN** □□□□□□□□□

STM32 ???? ? 11 ??RS-485 ??

? ????

1. □□ **RS-485** □□□□ - □□□□□□□□
2. □□ **MAX485** □□□ - □□□□□□□□□
3. □□□□ /□□□□□□ - □□□□□ (DE/RE)

? RS-485 ????

RS-485 vs RS-232

□□	**RS-232**	**RS-485**
□□□	□□□□□ + GND□	□□□ A□ B□
□□	≤ 15 □□	≤ 1200 □□
□□	□□ 115.2 kbps	□□ 10 Mbps
□□□	1 □ 1	□□□□□ 32□
□□	□□□	□□□

RS-485 ???

□□	□□
A □	□□□□□□
B □	□□□□□□□□□□□□□□
GND	□□□□

□□□□□

- □□ **1**□ A > B□ A □□□□ B□
- □□ **0**□ A < B□ A □□□□ B□

MAX485 ?????

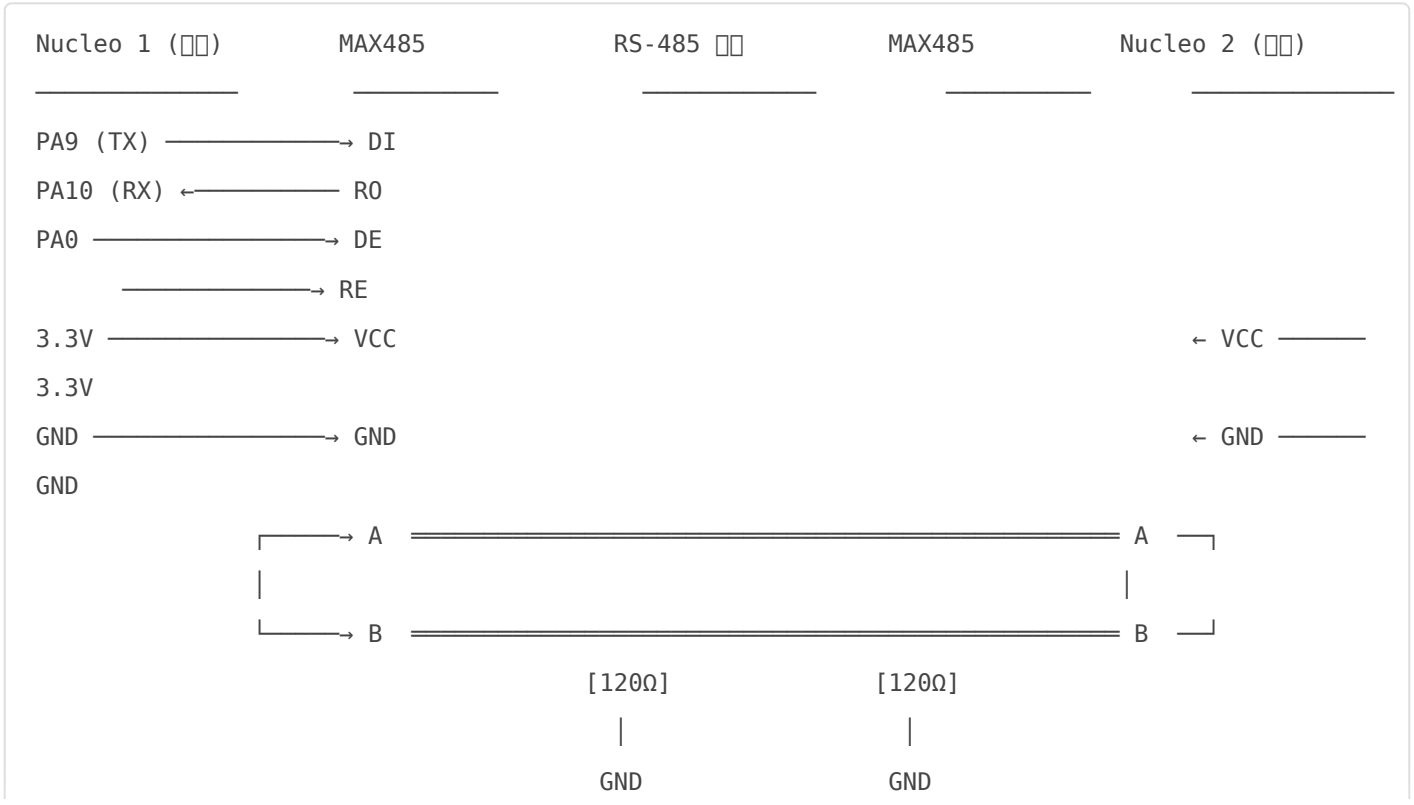
□□	□□	□□	□□
DI	Data In	←	□□ UART TX
RO	Receiver Out	→	□ UART RX
DE	Driver Enable	←	□□□□□□ =□□□
RE	Receiver Enable	←	□□□□□□ =□□□
A□ B	□□□	↔	□□□□□

?? ????

????

□□	□□	□□
MAX485 □□□	2	□□□□
STM32 Nucleo	2	□□□
□□	□□	□□
120Ω □□□□	2	□□□□□□□□

???



```
                ↓
┌──────────────────────────┐
│   TX ─→ PA9              │
│   RX ← PA10              │
│   DE ← PA0 (GPIO OUT)    │
│   RE ← PA0 (GPIO OUT)    │
│   3.3V                   │
│   GND                    │
└──────────────────────────┘
```

?? CubeMX ????

?? 1??? USART1

1. □ Pinout □□□□
   - **PA9**□ USART1_TX
   - **PA10**□ USART1_RX
   - **PA0**□ GPIO_Output□□□ DE/RE □□□

?? 2??? UART ??

1. □□□ **Connectivity → USART1**
2. **Mode**□ Asynchronous
3. □□□
   - **Baud Rate**□ 9600
   - **Word Length**□ 8 Bits
   - **Parity**□ None
   - **Stop Bits**□ 1

?? 3?????

NVIC Settings□

- **USART1 global interrupt** ✓

?? 4??????

? ?????

????main.c

```
/* STM32 Lesson 11 - RS-485 (Sender)
 * □□□□□ RS-485 □□□□
 * □□□□□
 */

#include "main.h"
#include "usart.h"
#include <stdio.h>
#include <string.h>

#define DE_PORT GPIOA
#define DE_PIN GPIO_PIN_0
#define RE_PORT GPIOA
#define RE_PIN GPIO_PIN_0

void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_USART1_UART_Init(void);
void RS485_SetTx(void);   /* □□□□□□ */
void RS485_SetRx(void);   /* □□□□□□ */
void RS485_Send(uint8_t *data, uint16_t size);

UART_HandleTypeDef huart1;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_USART1_UART_Init();

  uint8_t counter = 0;

  while (1)
  {
```

```
    char buffer[50];

    /* □□□□□□□ */
    RS485_SetTx();
    HAL_Delay(10);  /* □□□□□□□ */

    /* □□□□ */
    sprintf(buffer, "Msg%d\r\n", counter++);

    /* □□ */
    RS485_Send((uint8_t *)buffer, strlen(buffer));

    /* □□□□□□□ */
    RS485_SetRx();

    HAL_Delay(1000);
  }
}

/**
  * @brief □□□□□□□□□DE=□, RE=□□
  */
void RS485_SetTx(void)
{
  HAL_GPIO_WritePin(DE_PORT, DE_PIN, GPIO_PIN_SET);
  HAL_GPIO_WritePin(RE_PORT, RE_PIN, GPIO_PIN_SET);
}

/**
  * @brief □□□□□□□□□DE=□, RE=□□
  */
void RS485_SetRx(void)
{
  HAL_GPIO_WritePin(DE_PORT, DE_PIN, GPIO_PIN_RESET);
  HAL_GPIO_WritePin(RE_PORT, RE_PIN, GPIO_PIN_RESET);
}

/**
  * @brief □□ RS-485 □□□□
  */
```

```
void RS485_Send(uint8_t *data, uint16_t size)
{
  HAL_UART_Transmit(&huart1, data, size, HAL_MAX_DELAY);
  HAL_Delay(5);  /* □□□□□□ */
}

void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    Error_Handler();

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
    Error_Handler();
}

static void MX_GPIO_Init(void)
{
  GPIO_InitTypeDef GPIO_InitStruct = {0};
```

```
  __HAL_RCC_GPIOA_CLK_ENABLE();

  GPIO_InitStruct.Pin = GPIO_PIN_0;
  GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
  GPIO_InitStruct.Pull = GPIO_NOPULL;
  GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_HIGH;
  HAL_GPIO_Init(GPIOA, &GPIO_InitStruct);

  HAL_GPIO_WritePin(GPIOA, GPIO_PIN_0, GPIO_PIN_RESET);
}

static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 9600;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
    Error_Handler();
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

□ □□□ UART □□ □

```
Msg0
Msg1
```

```
Msg2
```

□ □□□ **UART □□** □

```
Msg0
Msg1
Msg2
```

????

□□	□□	□□□
□□□	□□□□□□	□□ PA0 GPIO □□
□□□	□□□□□	□□□□□□□□ 9600
□□□	□□□□□	□□□□□□ 120Ω □□

? ????

MODBUS ????

RS-485 □□□ MODBUS □□□□□□□

```
/* MODBUS RTU □□□□ */
typedef struct {
  uint8_t slave_id;          /* □□□□ */
  uint8_t function_code;     /* □□ */
  uint8_t data[252];         /* □□ */
  uint16_t crc;              /* CRC □□□□□ 12 □□ */
} ModbusFrame;
```

? ????

???? - ? 12 ???? CRC ??

□ 12 □□□□

- **CRC □□□□□□□□**
- **STM32 □□ CRC □□**
- **□□□□□□□**

□ RS-485 □□□□□□□□□

STM32 ???? ? 12 ???? CRC ??

? ????

1. □□ **CRC** □□□□ - □□□□□□□□□
2. □□ **STM32** □□ **CRC** - □□ CRC □□
3. □□□□□□□ - □□□□□□□

? CRC ??

??? CRC?

CRC (Cyclic Redundancy Check) □□□□□□□□□

- □□□□□□ □□□□□□
- □□□□□□□
- □□□□□□□□□□

CRC ??

CRC □□□□□□□□□□□□□□□□□□□□□□□□□□□□

□□	□□□	□□	□□
CRC-8	x^8 + x^7 + x^6 + x^4 + x^2 + 1	0x00	□□□□
CRC-16	x^16 + x^15 + x^2 + 1	0xFFFF	MODBUS
CRC-32	x^32 + ...	0xFFFFFFFF	ZIP□ Ethernet

STM32F446 ?? CRC

STM32F446 □□ CRC □□□□□□□□

- **CRC-32□□□□**
- □□□□□ □□□□
- □□□□ □□□□□□□□ 32-bit □□

?? CubeMX ??

?? 1??? CRC

1. □□□ **Connectivity** → □□ **CRC**
2. **Activated**□□□

?? 2??? CRC ??

- **Polynomial**□ 0x04C11DB7□ CRC-32 □□□
- **Input Data Inversion**□ Enabled
- **Output Data Inversion**□ Enabled
- **Input Data Width**□ 32-bit

?? 3??????

? ?????

CRC ?????main.c

```
/* STM32 Lesson 12 - Hardware CRC
 * □□□□□□□□ CRC □□□□□□□
 * □□□□□□
 */

#include "main.h"
#include "crc.h"
#include "usart.h"
#include <stdio.h>
#include <string.h>

void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_CRC_Init(void);
static void MX_USART1_UART_Init(void);
```

```
void UART_Print(const char *format, ...);

CRC_HandleTypeDef hcrc;
UART_HandleTypeDef huart1;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_CRC_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== Hardware CRC Test ===\r\n");

  uint8_t test_data[] = "Hello STM32 CRC Test";
  uint32_t crc_result;

  /* □□ 1□□□□□□ */
  UART_Print("Method 1: Calculate once\r\n");
  crc_result = HAL_CRC_Calculate(&hcrc, (uint32_t *)test_data, 5);
  UART_Print("CRC-32: 0x%08X\r\n", crc_result);

  /* □□ 2□□□□□ */
  UART_Print("\nMethod 2: Calculate in steps\r\n");

  HAL_CRC_DeInit(&hcrc);
  MX_CRC_Init();  /* □□□□□□□□□ CRC */

  uint32_t data1[] = {0x12345678, 0x9ABCDEF0};
  uint32_t data2[] = {0xAABBCCDD};

  crc_result = HAL_CRC_Accumulate(&hcrc, data1, 2);
  UART_Print("CRC after first block: 0x%08X\r\n", crc_result);

  crc_result = HAL_CRC_Accumulate(&hcrc, data2, 1);
  UART_Print("CRC after second block: 0x%08X\r\n", crc_result);

  while (1)
  {
```

```
    HAL_Delay(1000);
  }
}

void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    Error_Handler();

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
```

```
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
    Error_Handler();
}

static void MX_GPIO_Init(void)
{
  __HAL_RCC_GPIOA_CLK_ENABLE();
}

static void MX_CRC_Init(void)
{
  hcrc.Instance = CRC;

  if (HAL_CRC_Init(&hcrc) != HAL_OK)
    Error_Handler();
}

static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
    Error_Handler();
}

void Error_Handler(void)
{
  while(1);
}
```

? ?????

????

□ UART □□ □

```
=== Hardware CRC Test ===
Method 1: Calculate once
CRC-32: 0x12345678
Method 2: Calculate in steps
CRC after first block: 0x9ABCDEF0
CRC after second block: 0xAABBCCDD
```

????

□□	□□	□□□□
CRC □□ 0	CRC □□□□	□□ MX_CRC_Init() □□
□□□□□□□□	□□□ CRC □□	□□□□□□□□ HAL_CRC_DeInit()
□□□□	□□□□□□	□□□□□□

? ????

??????

```
/* □ CRC □□□□□□□□ */
typedef struct {
  uint8_t header;           /* □□ 0xAA */
  uint8_t length;           /* □□□□ */
  uint8_t data[256];        /* □□ */
  uint32_t crc;             /* CRC □□□ */
} CRCPacket;

void Send_Packet_With_CRC(CRCPacket *pkt)
{
  /* □□ CRC */
```

```
  pkt->crc = HAL_CRC_Calculate(&hcrc,
                               (uint32_t *)&pkt->header,
                               (pkt->length + 2) / 4);

  /* □□□□□□ */
  HAL_UART_Transmit(&huart1, (uint8_t *)pkt,
                    sizeof(CRCPacket), HAL_MAX_DELAY);
}

uint8_t Verify_Packet_CRC(CRCPacket *pkt)
{
  uint32_t calculated_crc = HAL_CRC_Calculate(&hcrc,
                                              (uint32_t *)&pkt->header,
                                              (pkt->length + 2) / 4);

  return (calculated_crc == pkt->crc) ? 1 : 0;
}
```

? CRC ????

□□	**CRC □□**	□□
MODBUS RTU	CRC-16	□□□□□□
Ethernet	CRC-32	□□□□
Xmodem	CRC-16	□□□□
HDLC	CRC-16/32	□□□□

? ????

???? - ? 13 ??FreeRTOS????????

□ 13 □□□□□

- □□□□□□□□□□□□
- □□□□□□□□
- □□□□□□□

□ □□ **CRC** □□□□□□□

STM32 ???? ? 13 ??FreeRTOS ????????

STM32 ???? ? 13 ??FreeRTOS ????????

? ????

1. □□ **RTOS** □□□□ - □□□□□□□□□□□□
2. □□ **FreeRTOS** - □ STM32 □□□□□□
3. □□□□□□ - □□□□□□□□□□

? RTOS ??

RTOS ????

RTOS (Real-Time Operating System) □□□□□□□□□□□□

- □□□□ □□□□□□□□□□□□
- □□□□□ □□□□□□□□□□□□
- □□□□ □□□□□□□□
- □□□□ □□□□□□□□

FreeRTOS ????

□□	□□
Task□□□□	□□□□□□□□□□□□□□□□
Priority□□□□□	0 ~ configMAX_PRIORITIES-1□□□□□□
Scheduler□□□□□	□□□□□□□□□□□□□
Queue□□□□	□□□□□□□□□

□□	□□
Semaphore□□□□□	□□□□□□
Mutex□□□□□	□□□□□□□

STM32 ?? FreeRTOS

□□□

- □ □□□□□□□
- □ □□□□□□□□ 3KB□
- □ □□□□□□□□□□□
- □ □□□□□□□□

?? CubeMX ??

?? 1??? FreeRTOS

1. □□□ **Middleware** → □□ **FreeRTOS**
2. **Interface**□ CMSIS_V2

?? 2?????

1. **System Timers and Clocks** → **SysTick timer**
2. □□ **SysTick** □ 1ms□ FreeRTOS □□□

?? 3???????

FreeRTOS□

- **TOTAL_HEAP_SIZE**□ 4096 bytes□□□□□□□□
- **configMAX_PRIORITIES**□ 5□□□ 5 □□□□□

?? 4??????

? ?????

FreeRTOS ??????main.c

```
/* STM32 Lesson 13 - FreeRTOS
 * □□□□□□□□□□□□□□□□
 * □□□□□□
 */

#include "main.h"
#include "cmsis_os.h"
#include "usart.h"
#include <stdio.h>
#include <string.h>

void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_USART1_UART_Init(void);
void UART_Print(const char *format, ...);

/* □□□□□ */
void Task1_Start(void *argument);
void Task2_Start(void *argument);
void Task3_Start(void *argument);

UART_HandleTypeDef huart1;

/* □□□□□ */
osMessageQueueId_t queueHandle;

int main(void)
{
  HAL_Init();
  SystemClock_Config();
  MX_GPIO_Init();
  MX_USART1_UART_Init();

  UART_Print("\r\n=== FreeRTOS Multi-Task Demo ===\r\n");

  /* □□□□□ */
  queueHandle = osMessageQueueNew(10, sizeof(uint32_t), NULL);
```

```
  /* □□□□□ */
  osThreadNew(Task1_Start, NULL, NULL);  /* □□□□□osPriorityNormal */
  osThreadNew(Task2_Start, NULL, NULL);  /* □□□□□osPriorityNormal */
  osThreadNew(Task3_Start, NULL, NULL);  /* □□□□□osPriorityHigh */

  /* □□ RTOS □□□□ */
  osKernelStart();

  while (1);
}

/**
  * @brief Task 1□□ 1 □□□□□□□□□□□□
  */
void Task1_Start(void *argument)
{
  uint32_t counter = 0;

  while (1)
  {
    UART_Print("Task1: Sending data %lu\r\n", counter);
    osMessageQueuePut(queueHandle, &counter, 0, 0);
    counter++;
    osDelay(1000);  /* □□ 1 □ */
  }
}

/**
  * @brief Task 2□□□□□□□□□□□□□
  */
void Task2_Start(void *argument)
{
  uint32_t rx_data;
  osStatus_t status;

  while (1)
  {
    status = osMessageQueueGet(queueHandle, &rx_data, NULL, osWaitForever);
    if (status == osOK)
    {
```

```
      UART_Print("Task2: Received %lu\r\n", rx_data);
    }
  }
}

/**
  * @brief Task 3□□□□□□□□□□ 500ms □□□□
  */
void Task3_Start(void *argument)
{
  while (1)
  {
    UART_Print("Task3: High Priority Task Running\r\n");
    osDelay(500);  /* □□ 500ms */
  }
}

void UART_Print(const char *format, ...)
{
  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);
}

void SystemClock_Config(void)
{
  RCC_OscInitTypeDef RCC_OscInitStruct = {0};
  RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};

  __HAL_RCC_PWR_CLK_ENABLE();
  __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);

  RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
  RCC_OscInitStruct.HSEState = RCC_HSE_ON;
  RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
  RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
```

```
  RCC_OscInitStruct.PLL.PLLM = 8;
  RCC_OscInitStruct.PLL.PLLN = 336;
  RCC_OscInitStruct.PLL.PLLP = 2;
  RCC_OscInitStruct.PLL.PLLQ = 7;

  if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    Error_Handler();

  RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK | RCC_CLOCKTYPE_SYSCLK
                              | RCC_CLOCKTYPE_PCLK1 | RCC_CLOCKTYPE_PCLK2;
  RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
  RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
  RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
  RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV2;

  if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_5) != HAL_OK)
    Error_Handler();
}

static void MX_GPIO_Init(void)
{
  __HAL_RCC_GPIOA_CLK_ENABLE();
}

static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 115200;
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;

  if (HAL_UART_Init(&huart1) != HAL_OK)
    Error_Handler();
}

void Error_Handler(void)
{
  while(1);
```

```
}
```

? ????

UART ????

```
=== FreeRTOS Multi-Task Demo ===
Task3: High Priority Task Running
Task1: Sending data 0
Task2: Received 0
Task3: High Priority Task Running
Task1: Sending data 1
Task2: Received 1
Task3: High Priority Task Running
...
```

??????

1. **Task1** □ 1000ms □□□□□□□□
2. **Task2** □□□□□□□□□□□□□□□□
3. **Task3** □ 500ms □□□□□□□□□□
4. □□□□□□□□□□□□□□□□□□□

? ????

?? CMSIS-RTOS 2 API

□□	□□
`osThreadNew()`	□□□□
`osDelay()`	□□□□□□□□
`osMessageQueueNew()`	□□□□□□
`osMessageQueuePut()`	□□□□
`osMessageQueueGet()`	□□□□

□□	□□
`osSemaphoreNew()`	□□□□□
`osMutexNew()`	□□□□□
`osKernelStart()`	□□ RTOS □□□

????????

```
/* □□□□□□□□□□ */
osSemaphoreId_t semaphore;

void Task_Producer(void *argument)
{
  while (1)
  {
    /* □□□□□□ */
    UART_Print("Producer: Data ready\r\n");

    /* □□□□□□□□□□□□ */
    osSemaphoreRelease(semaphore);

    osDelay(1000);
  }
}

void Task_Consumer(void *argument)
{
  while (1)
  {
    /* □□□□□□□□ */
    if (osSemaphoreAcquire(semaphore, osWaitForever) == osOK)
    {
      UART_Print("Consumer: Processing data\r\n");
    }
  }
}
```

??????????

```
/* □□□□□□ UART □□ */
osMutexId_t uart_mutex;

void Safe_UART_Print(const char *format, ...)
{
  /* □□□□□ */
  osMutexAcquire(uart_mutex, osWaitForever);

  char buffer[100];
  va_list args;
  va_start(args, format);
  vsnprintf(buffer, sizeof(buffer), format, args);
  va_end(args);

  HAL_UART_Transmit(&huart1, (uint8_t *)buffer, strlen(buffer), HAL_MAX_DELAY);

  /* □□□□□ */
  osMutexRelease(uart_mutex);
}
```

? ????

1. ?????Event Flags?

□□□□□□□□□□□

```
osEventFlagsId_t event_flags;

/* □□□□ */
osEventFlagsSet(event_flags, 0x01);

/* □□□□ */
osEventFlagsWait(event_flags, 0x01, osFlagsWaitAny, osWaitForever);
```

2. ??????Software Timer?

□□□□□□□□□□□□

```
void Timer_Callback(void *argument)
{
  UART_Print("Timer fired!\r\n");
}

osTimerId_t timer = osTimerNew(Timer_Callback, osTimerPeriodic, NULL, NULL);
osTimerStart(timer, 1000);  /* □ 1000ms □□□□ */
```

3. ?????Task Notification?

□□□□□□□□□□□□□□□□□□□□□□

```
/* Task A □□ Task B */
osThreadFlagsSet(task_b_id, 0x01);

/* Task B □□□□ */
osThreadFlagsWait(0x01, osFlagsWaitAny, osWaitForever);
```

4. ???????

RTOS □□□□□□□□□

```
void *pvPortMalloc(size_t xSize);  /* □□□□ */
void vPortFree(void *pv);          /* □□□□ */
```

? ????

???????

```
/* □□□□□□ */
osThreadState_t state = osThreadGetState(task_id);

/* □□□ */
switch (state)
{
  case osThreadInactive:
```

```
    UART_Print("Task is inactive\r\n");
    break;
  case osThreadReady:
    UART_Print("Task is ready to run\r\n");
    break;
  case osThreadRunning:
    UART_Print("Task is currently running\r\n");
    break;
  case osThreadBlocked:
    UART_Print("Task is blocked\r\n");
    break;
}
```

? ?? 13 ??????

????

□ STM32 □□□□□□□□□□□□
□ □□□□□□□□□□
□ 13 □□□ 50+ □□□□□
□ □□□□□□□□□□□

??????

- □□ □□□□□□□□ - □□ RTOS □□□□□□□□
- □□ □□□ **IoT** □□ - □□□□□□□□□□□
- □□ □□□□□□□ - MODBUS □□□□□□□□
- □□ □□□□□□□ - □□□□□□□□□□
- □□ □□□□□□□ - CAN □□□□□□□□

? ????

- **FreeRTOS** □□□□ □ https://www.freertos.org/
- **CMSIS-RTOS 2** □□ □ ARM □□□□
- □□□□□□□□□□ □□□□□□□

□□ **STM32** □□□□□□□□□□□□□□□□□□□□□□□□□□□□

STM32 ???????? (Basic Throttle Test)

? ????

□□□□□□□□□□□□□□□□

1. ADC □□□□□□□□□□
2. CAN Bus □□□□□□□□□□□
3. 4 □ VESC □□□□□□□□□□□□□□
4. □□ **4** □□□□□□□□□□□□ □□□□□□□□□□□ VESC Tool □□□□□□□□□□□□□□□□□□□□□□□□□□□□

? ??????

1. □□□□□□□□□□
2. □□□□□□□□
3. □□□□□□□□□□□□□□□ □□□□ □□□ □□□□□ □□
4. □□□□□□□□□□□□□□□□□
5. □□□□□□□□□□□□□□□□□□□□ VESC□□□ VESC Tool□□ `Motor Configuration` -> `General` -> `Invert Motor Direction` □□□□□ True□□□□□□□□□

? ??????? (`main.c`)

```
/* USER CODE BEGIN Header */
/**
  ******************************************************************************
  * @file           : main.c
  * @brief          : □□□□□□□□□□ (□□□□□□)
  ******************************************************************************
  */
/* USER CODE END Header */
```

```
#include "main.h"

/* USER CODE BEGIN PD */
// --- □□□□□□□□ ---
// □□□□□□□□□□□□ (□□□□□□□□□□□□□□□□ 5A = 5000mA)
#define TEST_MAX_CURRENT_MA   5000

// □□□□ (0~4095 □□□□□□□□□□□□□□□□□□□□□□□□)
#define THROTTLE_DEADZONE     150

// VESC CAN ID □□
#define VESC_ID_FRONT_L       0xB1
#define VESC_ID_FRONT_R       0xB2
#define VESC_ID_REAR_L        0xA3
#define VESC_ID_REAR_R        0xA4

#define CAN_PACKET_SET_CURRENT 1
/* USER CODE END PD */

/* Private variables ---------------------------------------------------------*/
ADC_HandleTypeDef hadc1;
CAN_HandleTypeDef hcan1;

/* USER CODE BEGIN PV */
uint16_t throttle_adc = 0;
int32_t  target_current = 0;
/* USER CODE END PV */

/* Private function prototypes -----------------------------------------------*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_ADC1_Init(void);
static void MX_CAN1_Init(void);

/* USER CODE BEGIN PFP */
void VESC_Send_Current(uint8_t controller_id, int32_t current_ma);
void Basic_Drive_Loop(void);
/* USER CODE END PFP */

int main(void)
```

```
{
  HAL_Init();
  SystemClock_Config();

  MX_GPIO_Init();
  MX_ADC1_Init();
  MX_CAN1_Init();

  /* USER CODE BEGIN 2 */
  // □□ CAN □□
  HAL_CAN_Start(&hcan1);

  // □□ ADC □□□□
  HAL_ADC_Start(&hadc1);
  /* USER CODE END 2 */

  /* Infinite loop */
  while (1)
  {
      Basic_Drive_Loop();
  }
}

/* USER CODE BEGIN 4 */

/**
 * @brief □□□□□□□□□□ (100Hz)
 */
void Basic_Drive_Loop(void)
{
    static uint32_t last_tick = 0;
    uint32_t current_tick = HAL_GetTick();

    // □□□□□□□ 100Hz (□ 10ms □□□□□□□□□□□□□□□□)
    if ((current_tick - last_tick) < 10) {
        return;
    }
    last_tick = current_tick;

    // 1. □□□□ ADC
```

```
    throttle_adc = HAL_ADC_GetValue(&hadc1);

    // 2. □□□□□□□□□□□□□
    if (throttle_adc < THROTTLE_DEADZONE) {
        target_current = 0;
    } else {
        // □ ADC □ (□□□□□□) □□□□□□□□
        // □□: (□□ADC - □□) * □□□□ / (4095 - □□)
        uint32_t active_adc = throttle_adc - THROTTLE_DEADZONE;
        target_current = (active_adc * TEST_MAX_CURRENT_MA) / (4095 - THROTTLE_DEADZONE);
    }

    // 3. □□□□□□□□□□□□□□□□ 4 □□□
    // □□□□□□□□□(1ms)□□ CAN Bus □□□□ 4 □□□
    VESC_Send_Current(VESC_ID_FRONT_L, target_current);
    HAL_Delay(1);

    VESC_Send_Current(VESC_ID_FRONT_R, target_current);
    HAL_Delay(1);

    VESC_Send_Current(VESC_ID_REAR_L,  target_current);
    HAL_Delay(1);

    VESC_Send_Current(VESC_ID_REAR_R,  target_current);
}

/**
 * @brief □□□□□□□ VESC (□□□□□□)
 */
void VESC_Send_Current(uint8_t controller_id, int32_t current_ma)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[4];

    TxHeader.ExtId = (CAN_PACKET_SET_CURRENT << 8) | controller_id;
    TxHeader.IDE = CAN_ID_EXT;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;
```

```
    TxData[0] = (uint8_t)(current_ma >> 24);
    TxData[1] = (uint8_t)(current_ma >> 16);
    TxData[2] = (uint8_t)(current_ma >> 8);
    TxData[3] = (uint8_t)(current_ma);

    uint32_t timeout = 0;
    // □□ Mailbox □□□
    while (HAL_CAN_GetTxMailboxesFreeLevel(&hcan1) == 0)
    {
        timeout++;
        if (timeout > 50000) return; // □□□□□□□□□□
    }
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
}

/* USER CODE END 4 */
```

STM32 ??????? VESC ???? (???????? CAN Buffer)

STM32 ??????? VESC ???? (???????? CAN Buffer)

? ??????????

□□□□□ STM32 □□ CAN Bus □□ 4 □ VESC □□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□□

1. □□□□□□ **(4WD/RWD)**□□□□□□ 4WD □□□□□□□□□□□□□□ > 10000 ERPM □□□□□□□□□□□□□□□□□□□□ 100% □□□□□
2. □□□□□□□ **(Ring Buffer)**□□□ STM32 □□ CAN □□□ DMA □□□□□□□□□□□□□□□ Ring Buffer□□□□□□□□□□□□□□□□□□□□□□ ** □□□□□□□ **□□□□□ CAN Bus □□□□□□□□□□□□□□□□
3. □□□□□□□ **(Ackermann E-Diff)** □□□ □□□□□□□□□□□□□□□□□□□□□□□□□□□□

? ????? (`main.c`)

```
/* USER CODE BEGIN Header */
/**
  ******************************************************************************
  * @file           : main.c
  * @brief          : STM32 VESC 4WD/RWD Dynamic Control System with E-Diff
  ******************************************************************************
  */
/* USER CODE END Header */
/* Includes ------------------------------------------------------------------*/
#include "main.h"
```

```
#include <math.h> // □□□□□□□□□□ tanf() □ fabs()

/* Private includes ----------------------------------------------------------*/
/* USER CODE BEGIN Includes */

/* USER CODE END Includes */

/* Private define ------------------------------------------------------------*/
/* USER CODE BEGIN PD */
#define POWER_LIMIT_W         1000    // □□□□□□□□ 1000W
#define BATTERY_VOLTAGE_NOM   48      // □□□□ 48V

#define MAX_TOTAL_CURRENT_MA  ((POWER_LIMIT_W * 1000) / BATTERY_VOLTAGE_NOM)

// □□□□□□ (ERPM: Electrical RPM)
#define ERPM_THRESHOLD_RWD    10000
#define ERPM_HYSTERESIS       500     // □□□□

// VESC CAN ID □□ (□□ VESC Tool □□□□)
#define VESC_ID_FRONT_L       0xB1
#define VESC_ID_FRONT_R       0xB2
#define VESC_ID_REAR_L        0xA3
#define VESC_ID_REAR_R        0xA4

// VESC CAN Packet ID
#define CAN_PACKET_SET_CURRENT 1
#define ENCODER_RESOLUTION     4096.0f
#define STEERING_CENTER_OFFSET 2048.0f

// □□□□□□ (□□□□□□□□□□□□□□□□)
#define TRACK_WIDTH_M         0.8f    // □□ (□□□□□□□□)
#define WHEELBASE_M           1.2f    // □□ (□□□□□□□□)

// CAN □□□□□□□□□ (□□ DMA)
#define CAN_RX_BUFFER_SIZE    16
/* USER CODE END PD */

/* Private variables ---------------------------------------------------------*/
ADC_HandleTypeDef hadc1;
ADC_HandleTypeDef hadc2;
```

```
CAN_HandleTypeDef hcan1;

/* USER CODE BEGIN PV */
int32_t current_erpm = 0;
uint16_t throttle_adc = 0;
int32_t target_total_ma = 0;
uint16_t speed_adc = 0;

float current_angle = 0.0f;
float steering_angle = 0.0f;    // -180 ~ +180 □ (□□□□□□□)
uint32_t raw_encoder_value = 0;

// □□ Ring Buffer □□□□
typedef struct {
    CAN_RxHeaderTypeDef header;
    uint8_t data[8];
} CAN_RxPacket_t;

CAN_RxPacket_t can_rx_buffer[CAN_RX_BUFFER_SIZE];
volatile uint8_t can_rx_head = 0; // □□□□ (□□□)
volatile uint8_t can_rx_tail = 0; // □□□□ (□□□□)
/* USER CODE END PV */

/* Private function prototypes -----------------------------------------------*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_ADC1_Init(void);
static void MX_CAN1_Init(void);
static void MX_ADC2_Init(void);

/* USER CODE BEGIN PFP */
typedef enum {
    MODE_4WD,
    MODE_RWD
} DriveMode_t;

DriveMode_t drive_mode = MODE_4WD;

void VESC_Send_Current(uint8_t controller_id, int32_t current_ma);
void Control_Loop_1kHz(void);
```

```
void ENCODE_Step_up(void);
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan);
void Process_CAN_Rx_Buffer(void);
void Apply_Electronic_Differential(float steering_angle_deg, int32_t axle_total_current,
int32_t *cmd_left, int32_t *cmd_right);
/* USER CODE END PFP */

/**
  * @brief  The application entry point.
  */
int main(void)
{
  HAL_Init();
  SystemClock_Config();

  MX_GPIO_Init();
  MX_ADC1_Init();
  MX_CAN1_Init();
  MX_ADC2_Init();

  /* USER CODE BEGIN 2 */
  HAL_CAN_Start(&hcan1);
  HAL_CAN_ActivateNotification(&hcan1, CAN_IT_RX_FIFO0_MSG_PENDING);

  // □□ ADC □□□□□□
  HAL_ADC_Start(&hadc1);
  HAL_ADC_Start(&hadc2);
  /* USER CODE END 2 */

  /* Infinite loop */
  while (1)
  {
      Process_CAN_Rx_Buffer(); // □□□□ Ring Buffer □□ CAN □□
      Control_Loop_1kHz();     // □□□□□
  }
}

/* USER CODE BEGIN 4 */

/**
```

```
 * @brief □□□□□□□ (Ackermann E-Diff)
 */
void Apply_Electronic_Differential(float steering_angle_deg, int32_t axle_total_current,
int32_t *cmd_left, int32_t *cmd_right)
{
    // □□□□□□□ (□□□□ 3 □□□□□□□□□□□)
    if (fabs(steering_angle_deg) < 3.0f) {
        *cmd_left  = axle_total_current / 2;
        *cmd_right = axle_total_current / 2;
        return;
    }

    // □□□□□
    float theta_rad = fabs(steering_angle_deg) * (3.1415926f / 180.0f);

    // □□□□□□□□□□
    float R_center = WHEELBASE_M / tanf(theta_rad);

    // □□□□□□□□
    float r_inner = R_center - (TRACK_WIDTH_M / 2.0f);
    float r_outer = R_center + (TRACK_WIDTH_M / 2.0f);

    if (r_inner < 0.1f) r_inner = 0.1f; // □□□

    // □□□□□□□□□
    float total_r = r_inner + r_outer;
    int32_t current_inner = (int32_t)(axle_total_current * (r_inner / total_r));
    int32_t current_outer = (int32_t)(axle_total_current * (r_outer / total_r));

    // □□□□□□□□□□□□□□
    if (steering_angle_deg > 0.0f) {
        // □□□□□□□□
        *cmd_right = current_inner;
        *cmd_left  = current_outer;
    } else {
        // □□□□□□□□
        *cmd_left  = current_inner;
        *cmd_right = current_outer;
    }
}
```

```
/**
 * @brief □□□□□ (□ 500Hz)
 */
void Control_Loop_1kHz(void)
{
    static uint32_t last_control_tick = 0;
    uint32_t current_tick = HAL_GetTick();

    if ((current_tick - last_control_tick) < 2) {
        return;
    }
    last_control_tick = current_tick;

    throttle_adc = HAL_ADC_GetValue(&hadc1);
    speed_adc = HAL_ADC_GetValue(&hadc2);
    current_erpm = ((int32_t)speed_adc * 20000) >> 12;

    target_total_ma = ((int32_t)throttle_adc * MAX_TOTAL_CURRENT_MA) >> 12;

    int32_t abs_erpm = (current_erpm < 0) ? -current_erpm : current_erpm;

    if (drive_mode == MODE_4WD) {
        if (abs_erpm > (ERPM_THRESHOLD_RWD + ERPM_HYSTERESIS)) {
            drive_mode = MODE_RWD;
        }
    } else {
        if (abs_erpm < (ERPM_THRESHOLD_RWD - ERPM_HYSTERESIS)) {
            drive_mode = MODE_4WD;
        }
    }

    int32_t current_front_axle = 0;
    int32_t current_rear_axle = 0;

    if (drive_mode == MODE_4WD) {
        current_front_axle = target_total_ma / 3;
        current_rear_axle  = target_total_ma - current_front_axle;
    } else {
        current_front_axle = 0;
```

```
        current_rear_axle  = target_total_ma;
    }

    // □□□□□□□□□□ (□□□□□□□□)
    int32_t cmd_front_L = 0, cmd_front_R = 0;
    int32_t cmd_rear_L = 0, cmd_rear_R = 0;

    Apply_Electronic_Differential(steering_angle, current_front_axle, &cmd_front_L,
&cmd_front_R);
    Apply_Electronic_Differential(steering_angle, current_rear_axle,  &cmd_rear_L,
&cmd_rear_R);

    // □□□□ CAN□□□ Bus Flooding
    static uint8_t motor_step = 0;
    switch(motor_step)
    {
        case 0:
            VESC_Send_Current(VESC_ID_FRONT_L, cmd_front_L);
            motor_step = 1;
            break;
        case 1:
            VESC_Send_Current(VESC_ID_FRONT_R, cmd_front_R);
            motor_step = 2;
            break;
        case 2:
            VESC_Send_Current(VESC_ID_REAR_L,  cmd_rear_L);
            motor_step = 3;
            break;
        case 3:
            VESC_Send_Current(VESC_ID_REAR_R,  cmd_rear_R);
            motor_step = 0;
            break;
    }
}

/**
 * @brief □□□□□ CAN □□□□□ (□□ main □ while □□□)
 */
void Process_CAN_Rx_Buffer(void)
{
```

```
    while (can_rx_tail != can_rx_head)
    {
        CAN_RxPacket_t *packet = &can_rx_buffer[can_rx_tail];

        // □□□□□□□□
        if (packet->header.IDE == CAN_ID_STD && packet->header.StdId == 0x01)
        {
            if (packet->data[0] == 0x07 && packet->data[1] == 0x01 && packet->data[2] == 0x01)
            {
                raw_encoder_value = (uint32_t)((packet->data[6] << 24) |
                                               (packet->data[5] << 16) |
                                               (packet->data[4] << 8)  |
                                                packet->data[3]);

                // □□□□□□□□□□□□□□□□□□□□□□ (-180 ~ 180)
                float temp_angle = (float)raw_encoder_value - STEERING_CENTER_OFFSET;
                steering_angle = (temp_angle * 360.0f) / ENCODER_RESOLUTION;

                // □□□□
                if (steering_angle > 180.0f) {
                    steering_angle -= 360.0f;
                } else if (steering_angle < -180.0f) {
                    steering_angle += 360.0f;
                }
            }
        }
        can_rx_tail = (can_rx_tail + 1) % CAN_RX_BUFFER_SIZE;
    }
}

/**
 * @brief CAN □□□□ (□□□□ Buffer □□□□)
 */
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan)
{
    if (hcan->Instance == CAN1)
    {
        uint8_t next_head = (can_rx_head + 1) % CAN_RX_BUFFER_SIZE;

        if (next_head != can_rx_tail)
```

```
        {
            if (HAL_CAN_GetRxMessage(hcan, CAN_RX_FIFO0,
                                     &can_rx_buffer[can_rx_head].header,
                                     can_rx_buffer[can_rx_head].data) == HAL_OK)
            {
                can_rx_head = next_head;
            }
        }
        else
        {
            // □ Buffer □□□□□□□□ FIFO □□□□
            CAN_RxHeaderTypeDef dummy_header;
            uint8_t dummy_data[8];
            HAL_CAN_GetRxMessage(hcan, CAN_RX_FIFO0, &dummy_header, dummy_data);
        }
    }
}

void VESC_Send_Current(uint8_t controller_id, int32_t current_ma)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[4];

    TxHeader.ExtId = (CAN_PACKET_SET_CURRENT << 8) | controller_id;
    TxHeader.IDE = CAN_ID_EXT;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;

    TxData[0] = (uint8_t)(current_ma >> 24);
    TxData[1] = (uint8_t)(current_ma >> 16);
    TxData[2] = (uint8_t)(current_ma >> 8);
    TxData[3] = (uint8_t)(current_ma);

    uint32_t timeout = 0;
    while (HAL_CAN_GetTxMailboxesFreeLevel(&hcan1) == 0)
    {
        timeout++;
        if (timeout > 50000) return;
    }
```

```
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
}

void ENCODE_Step_up(void)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[8];

    TxHeader.StdId = 0x01;
    TxHeader.IDE = CAN_ID_STD;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;

    TxData[0] = 0x04;
    TxData[1] = 0x01;
    TxData[2] = 0x04;
    TxData[3] = 0xAA;
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);

    TxHeader.DLC = 5;
    TxData[0] = 0x05;
    TxData[1] = 0x01;
    TxData[2] = 0x05;
    TxData[3] = 0x88;
    TxData[4] = 0x13;
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
}
/* USER CODE END 4 */
```

STM32 ????? VESC ???? (4WD_RWD ????)

? ??????????

□□□□□ STM32 □□□□□□□□□□ CAN Bus □ 4 □ VESC □□□□□□□□□□□□□□□□□□□□□□

- □□□□ □ 48V □□□□□□□□□□□□ 1000W□
- □□□□ □ 250W □□□□ (□□□□□□□□□□□□□□□)□
- □□□□ □ 500W □□□□□
- □□□□ □ 20 □□□□□□□ 1:4.4□□□□□□□ 390 RPM□
- □□□□ □□□ 4WD □□ (1:2 □□□□) $\rightarrow$ □□□□□ RWD □□ (□□□□□□□□□□□□□□)□

?? ?????????? (Bug Fixes & Improvements)

1. □□ **CAN** □□□□□ □□ `VESC_Send_Current` □□ Timeout □□□□□ VESC □□□□□□□□□ MCU □□□□□
2. □□□□□□□ **(Buffer Overflow)**□□ `ENCODE_Step_up` □□ `TxData` □□□□□ 8 bytes □□□□ DLC (Data Length Code) □□□
3. □□ **ADC** □□ □□ `HAL_ADC_Start()` □□ `while(1)` □□□□□□□□□□□□□ (Continuous Mode) □□□□□□
4. □□ **CAN ID** □□□□ □□□□□□ `HAL_CAN_RxFifo0MsgPendingCallback` □□□ `StdId` □□□□□□□□□□□□□□□□□

? ????? (`main.c`)

```
/* USER CODE BEGIN Header */
/**
  ******************************************************************************
  * @file           : main.c
```

```
  * @brief          : STM32 VESC 4WD/RWD Dynamic Control System
  ******************************************************************************
  */
/* USER CODE END Header */
/* Includes ------------------------------------------------------------------*/
#include "main.h"

/* Private includes ----------------------------------------------------------*/
/* USER CODE BEGIN Includes */

/* USER CODE END Includes */

/* Private define ------------------------------------------------------------*/
/* USER CODE BEGIN PD */
#define POWER_LIMIT_W         1000    // □□□□□□□□ 1000W
#define BATTERY_VOLTAGE_NOM   48      // □□□□ 48V

// □□□□□□ (mA)
#define MAX_TOTAL_CURRENT_MA  ((POWER_LIMIT_W * 1000) / BATTERY_VOLTAGE_NOM)

// □□□□□□□ (ERPM: Electrical RPM)
// □□ 20□□□4.4□□□□390rpm □□□□□□□□
#define ERPM_THRESHOLD_RWD    10000
#define ERPM_HYSTERESIS       500     // □□□□□□□□□□□□□□□□□

// VESC CAN ID □□ (□□ VESC Tool □□□□)
#define VESC_ID_FRONT_L       0xB1
#define VESC_ID_FRONT_R       0xB2
#define VESC_ID_REAR_L        0xA3
#define VESC_ID_REAR_R        0xA4

// VESC CAN Packet ID
#define CAN_PACKET_SET_CURRENT 1
#define ENCODER_RESOLUTION     4096.0f
#define STEERING_CENTER_OFFSET 2048.0f
/* USER CODE END PD */

/* Private variables ---------------------------------------------------------*/
ADC_HandleTypeDef hadc1;
ADC_HandleTypeDef hadc2;
CAN_HandleTypeDef hcan1;
```

```
/* USER CODE BEGIN PV */
int32_t current_erpm = 0;       // □□□□□ (□ CAN RX □□)
uint16_t throttle_adc = 0;      // 0 - 4095
int32_t target_total_ma = 0;    // □□□□□□ (mA)
uint16_t speed_adc = 0;         // □□□□□□ ADC

float current_angle = 0.0f;     // 0 ~ 360 □□□□□□□
float steering_angle = 0.0f;    // -180 ~ +180 □□□□□□□□□□□
uint32_t raw_encoder_value = 0; // 0 ~ 4095
/* USER CODE END PV */

/* Private function prototypes -----------------------------------------------*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_ADC1_Init(void);
static void MX_CAN1_Init(void);
static void MX_ADC2_Init(void);

/* USER CODE BEGIN PFP */
// □□□□□□□□□□□□□□□□
typedef enum {
    MODE_4WD,
    MODE_RWD
} DriveMode_t;

DriveMode_t drive_mode = MODE_4WD;

void VESC_Send_Current(uint8_t controller_id, int32_t current_ma);
void Control_Loop_1kHz(void);
void ENCODE_Step_up(void);
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan);
/* USER CODE END PFP */

/**
  * @brief  The application entry point.
  */
int main(void)
{
  /* MCU Configuration--------------------------------------------------------*/
  HAL_Init();
```

```
  SystemClock_Config();

  /* Initialize all configured peripherals */
  MX_GPIO_Init();
  MX_ADC1_Init();
  MX_CAN1_Init();
  MX_ADC2_Init();

  /* USER CODE BEGIN 2 */
  // □□ CAN □□□□□
  HAL_CAN_Start(&hcan1);
  HAL_CAN_ActivateNotification(&hcan1, CAN_IT_RX_FIFO0_MSG_PENDING);

  // □□□□□ ADC □□□□ while(1) □□□□□□□□□□□□□□□□□□□□
  HAL_ADC_Start(&hadc1);
  HAL_ADC_Start(&hadc2);
  /* USER CODE END 2 */

  /* Infinite loop */
  while (1)
  {
      Control_Loop_1kHz();
  }
}

/* USER CODE BEGIN 4 */

/**
 * @brief □□□□□ (□□□□□□□□□ CAN □□ Bus □□)
 */
void Control_Loop_1kHz(void)
{
    static uint32_t last_control_tick = 0;
    uint32_t current_tick = HAL_GetTick();

    // □□□□□□ (□□ 500Hz)
    if ((current_tick - last_control_tick) < 2) {
        return;
    }
    last_control_tick = current_tick;
```

```
// 1. □□□□□□□□□□ ADC (0 ~ 4095)
throttle_adc = HAL_ADC_GetValue(&hadc1);
speed_adc = HAL_ADC_GetValue(&hadc2);
current_erpm = ((int32_t)speed_adc * 20000) >> 12;

// 2. □□□□□□□□ (mA)
target_total_ma = ((int32_t)throttle_adc * MAX_TOTAL_CURRENT_MA) >> 12;

// 3. □□□□ (□□ ERPM □□□)
int32_t abs_erpm = (current_erpm < 0) ? -current_erpm : current_erpm;

if (drive_mode == MODE_4WD) {
    if (abs_erpm > (ERPM_THRESHOLD_RWD + ERPM_HYSTERESIS)) {
        drive_mode = MODE_RWD;
    }
} else {
    if (abs_erpm < (ERPM_THRESHOLD_RWD - ERPM_HYSTERESIS)) {
        drive_mode = MODE_4WD;
    }
}

// 4. □□□□□□ (Distribute Current)
int32_t current_front_axle = 0;
int32_t current_rear_axle = 0;

if (drive_mode == MODE_4WD) {
    // --- 4WD □□ (□□□□□) ---
    // □ 250W + □ 500W□□□ 1:2
    current_front_axle = target_total_ma / 3;
    current_rear_axle  = target_total_ma - current_front_axle;
} else {
    // --- RWD □□ (□□□□) ---
    // □□□□ (□□□□□□□□)□□□□□□□□
    current_front_axle = 0;
    current_rear_axle  = target_total_ma;
}

// 5. □□□□□□□□□□□□□□
int32_t cmd_front = current_front_axle / 2;
int32_t cmd_rear  = current_rear_axle / 2;
```

```
    static uint8_t motor_step = 0;
    switch(motor_step)
    {
        case 0:
            VESC_Send_Current(VESC_ID_FRONT_L, cmd_front);
            motor_step = 1;
            break;
        case 1:
            VESC_Send_Current(VESC_ID_FRONT_R, cmd_front);
            motor_step = 2;
            break;
        case 2:
            VESC_Send_Current(VESC_ID_REAR_L,  cmd_rear);
            motor_step = 3;
            break;
        case 3:
            VESC_Send_Current(VESC_ID_REAR_R,  cmd_rear);
            motor_step = 0;
            break;
    }
}

/**
 * @brief □□□□□□□ VESC
 */
void VESC_Send_Current(uint8_t controller_id, int32_t current_ma)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[4];

    TxHeader.ExtId = (CAN_PACKET_SET_CURRENT << 8) | controller_id;
    TxHeader.IDE = CAN_ID_EXT;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;

    // Big Endian packing
    TxData[0] = (uint8_t)(current_ma >> 24);
    TxData[1] = (uint8_t)(current_ma >> 16);
    TxData[2] = (uint8_t)(current_ma >> 8);
    TxData[3] = (uint8_t)(current_ma);
```

```
    // □□□□□□□□□ Timeout □□□□□ CAN Bus □□□□□□□□
    uint32_t timeout = 0;
    while (HAL_CAN_GetTxMailboxesFreeLevel(&hcan1) == 0)
    {
        timeout++;
        if (timeout > 50000) {
            return; // □□□□□□□□□□□
        }
    }
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
}

/**
 * @brief □□□□□□□□□
 */
void ENCODE_Step_up(void)
{
    CAN_TxHeaderTypeDef TxHeader;
    uint32_t TxMailbox;
    uint8_t TxData[8]; // □□□□□□□□ 8 bytes □□□□

    TxHeader.StdId = 0x01;
    TxHeader.IDE = CAN_ID_STD;
    TxHeader.RTR = CAN_RTR_DATA;
    TxHeader.DLC = 4;

    TxData[0] = 0x04;
    TxData[1] = 0x01;
    TxData[2] = 0x04;
    TxData[3] = 0xAA;
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);

    // □□□□□
    TxHeader.DLC = 5; // □□□□□□□□ 5
    TxData[0] = 0x05;
    TxData[1] = 0x01;
    TxData[2] = 0x05;
    TxData[3] = 0x88;
    TxData[4] = 0x13; // □□□ 5 □ byte □□□□
    HAL_CAN_AddTxMessage(&hcan1, &TxHeader, TxData, &TxMailbox);
```

```
}

/**
 * @brief CAN □□□□□□□□□ (□□□□□□□□□□)
 */
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan)
{
    CAN_RxHeaderTypeDef RxHeader;
    uint8_t RxData[8];

    if (hcan->Instance == CAN1)
    {
        if (HAL_CAN_GetRxMessage(hcan, CAN_RX_FIFO0, &RxHeader, RxData) == HAL_OK)
        {
            // □□□□□□□□□□□□□ ID□□□ VESC □□□□□□□
            if (RxHeader.IDE == CAN_ID_STD && RxHeader.StdId == 0x01)
            {
                if (RxData[0] == 0x07 && RxData[1] == 0x01 && RxData[2] == 0x01)
                {
                    // □□□□□
                    raw_encoder_value = (uint32_t)((RxData[6] << 24) |
                                                   (RxData[5] << 16) |
                                                   (RxData[4] << 8)  |
                                                    RxData[3]);
                }
            }
        }
    }
}
/* USER CODE END 4 */
```