????CODE??

STM32 CAN ??? + UART ????

□□□□ : TTR9.5_eco_VCU

> “ □□□□□□□□□ STM32F4 □□□□□□□□□□□□□□□□ CAN1□ CAN2 □ UART □□□□□

? ??

?????

???????

```
/* main() □□□□□□□□□□ */
MX_GPIO_Init();           // GPIO □□□
MX_CAN1_Init();          // CAN1 □□□
MX_CAN2_Init();          // CAN2 □□□
MX_USART1_UART_Init();   // UART1 □□□
MX_USART6_UART_Init();   // UART6 □□□
```

image

□□□□□□□□□

- GPIO □□□□□□□□□□□□□□□□□□□
- CAN □ UART □□□□□□□□

CAN1 ????

1?? **CAN1 ?????** (`MX_CAN1_Init`)

```
static void MX_CAN1_Init(void)
{
  hcan1.Instance = CAN1;
  hcan1.Init.Prescaler = 5;              // □□□□□□□□□□ CAN □□
  hcan1.Init.Mode = CAN_MODE_NORMAL;     // □□□□
  hcan1.Init.SyncJumpWidth = CAN_SJW_1TQ;
  hcan1.Init.TimeSeg1 = CAN_BS1_15TQ;    // □□□□□ 1
  hcan1.Init.TimeSeg2 = CAN_BS2_2TQ;     // □□□□□ 2
  hcan1.Init.TimeTriggeredMode = DISABLE;
  hcan1.Init.AutoBusOff = DISABLE;
  hcan1.Init.AutoWakeUp = DISABLE;
  hcan1.Init.AutoRetransmission = DISABLE;
  hcan1.Init.ReceiveFifoLocked = DISABLE;
  hcan1.Init.TransmitFifoPriority = DISABLE;

  if (HAL_CAN_Init(&hcan1) != HAL_OK)
  {
    Error_Handler();
  }
}
```

□□□□□

□□	□	□□
`Prescaler`	5	□□ CAN □□□□□
`TimeSeg1`	15TQ	□□□ 1 = 15 □□□
`TimeSeg2`	2TQ	□□□ 2 = 2 □□□
`SyncJumpWidth`	1TQ	□□□□□□

□□□□□□

```
CAN_BaudRate = APB1_Clock / (Prescaler × (1 + TimeSeg1 + TimeSeg2))
             = 45MHz / (5 × (1 + 15 + 2))
             = 45MHz / (5 × 18)
             = 500 kbps
```

2?? **CAN1 ?????**

```
CAN_FilterTypeDef sFilterConfig;

// □□ CAN1 □□□
sFilterConfig.FilterBank = 0;               // □□□ 0 □□□□
sFilterConfig.FilterMode = CAN_FILTERMODE_IDMASK;
sFilterConfig.FilterScale = CAN_FILTERSCALE_32BIT;
sFilterConfig.FilterIdHigh = 0x0000;
sFilterConfig.FilterIdLow = 0x0000;
sFilterConfig.FilterMaskIdHigh = 0x0000;
sFilterConfig.FilterMaskIdLow = 0x0000;
sFilterConfig.FilterFIFOAssignment = CAN_RX_FIFO0;
sFilterConfig.FilterActivation = ENABLE;
sFilterConfig.SlaveStartFilterBank = 14;   // CAN2 □□ 14 □□□□□□

HAL_CAN_ConfigFilter(&hcan1, &sFilterConfig);
HAL_CAN_Start(&hcan1);
HAL_CAN_ActivateNotification(&hcan1, CAN_IT_RX_FIFO0_MSG_PENDING);
```

□□□□□□

- **FilterBank 0-13**□ CAN1 □□
- **FilterBank 14-27**□ CAN2 □□□ Slave □□□
- **FilterIdLow = 0x0000 + FilterMaskIdLow = 0x0000**□□□□□□□
- **CAN_RX_FIFO0**□□□□□□□□ FIFO0
- **ActivateNotification**□□□ FIFO0 □□□□□

3?? **CAN1 ????** (`CAN1_Send_Test`)

```
void CAN1_Send_Test(uint8_t counter)
{
    // □□□□□□□□
```

```
    TxHeader1.StdId = 0x101;           // CAN ID：0x101
    TxHeader1.RTR = CAN_RTR_DATA;      // □□□□□□□□□□
    TxHeader1.IDE = CAN_ID_STD;        // □□ ID（11-bit）
    TxHeader1.DLC = 8;                 // □□□□□8 bytes
    TxHeader1.TransmitGlobalTime = DISABLE;

    // □□ 8 bytes □□
    TxData1[0] = counter;              // byte 0□□□□
    TxData1[1] = 0x22;
    TxData1[2] = 0x33;
    TxData1[3] = 0x00;
    TxData1[4] = 0x00;
    TxData1[5] = 0x00;
    TxData1[6] = 0x00;
    TxData1[7] = 0x00;

    // □□□□□□□□□□□□
    if (HAL_CAN_GetTxMailboxesFreeLevel(&hcan1) > 0)
    {
        if (HAL_CAN_AddTxMessage(&hcan1, &TxHeader1, TxData1, &TxMailbox1) != HAL_OK)
        {
            // □□□□□□
        }
    }
}
```

□□□□

1. □□ `TxHeader1` □□
2. □□ `TxData1` □□□□ 8 bytes□
3. □□□□□□□□□□
4. □□ `HAL_CAN_AddTxMessage()` □□

□□□□□□□

```
while(1)
{
    if (HAL_GetTick() - last_uart_tick >= 300)
    {
        Send_uart[86]++;
        HAL_UART_Transmit(&huart1, Send_uart, sendData_lenght + 3, 1000);
```

```
        CAN1_Send_Test(test_count++);  // ← □ 300ms □□□□ CAN □□
        last_uart_tick = HAL_GetTick();
    }
}
```

CAN2 ????

1?? **CAN2 ?????** (`MX_CAN2_Init`)

```
static void MX_CAN2_Init(void)
{
  hcan2.Instance = CAN2;
  hcan2.Init.Prescaler = 5;
  hcan2.Init.Mode = CAN_MODE_NORMAL;
  hcan2.Init.SyncJumpWidth = CAN_SJW_1TQ;
  hcan2.Init.TimeSeg1 = CAN_BS1_15TQ;
  hcan2.Init.TimeSeg2 = CAN_BS2_2TQ;
  hcan2.Init.TimeTriggeredMode = DISABLE;
  hcan2.Init.AutoBusOff = DISABLE;
  hcan2.Init.AutoWakeUp = DISABLE;
  hcan2.Init.AutoRetransmission = DISABLE;
  hcan2.Init.ReceiveFifoLocked = DISABLE;
  hcan2.Init.TransmitFifoPriority = DISABLE;

  if (HAL_CAN_Init(&hcan2) != HAL_OK)
  {
    Error_Handler();
  }
}
```

 CAN1 □□□□□□□

2?? **CAN2 ?????**

```
// □□ CAN2 □□□
sFilterConfig.FilterBank = 14;              // ← □□□ 14 □□□□□Slave □□□□□
sFilterConfig.FilterMode = CAN_FILTERMODE_IDMASK;
```

```
sFilterConfig.FilterScale = CAN_FILTERSCALE_32BIT;
sFilterConfig.FilterIdHigh = 0x0000;
sFilterConfig.FilterIdLow = 0x0000;
sFilterConfig.FilterMaskIdHigh = 0x0000;
sFilterConfig.FilterMaskIdLow = 0x0000;
sFilterConfig.FilterFIFOAssignment = CAN_RX_FIFO0;
sFilterConfig.FilterActivation = ENABLE;

HAL_CAN_ConfigFilter(&hcan2, &sFilterConfig);
HAL_CAN_Start(&hcan2);
HAL_CAN_ActivateNotification(&hcan2, CAN_IT_RX_FIFO0_MSG_PENDING);
```

CAN1 vs CAN2 □□□□□□

```
CAN1: FilterBank 0-13   (14 □□□□)
CAN2: FilterBank 14-27  (14 □□□□□Slave □□)
```

UART ????

1?? **UART1 ???** (Titania ????)

```
static void MX_USART1_UART_Init(void)
{
  huart1.Instance = USART1;
  huart1.Init.BaudRate = 9600;              // ← □□□□9600 bps
  huart1.Init.WordLength = UART_WORDLENGTH_8B;
  huart1.Init.StopBits = UART_STOPBITS_1;
  huart1.Init.Parity = UART_PARITY_NONE;
  huart1.Init.Mode = UART_MODE_TX_RX;       // □□□□
  huart1.Init.HwFlowCtl = UART_HWCONTROL_NONE;
  huart1.Init.OverSampling = UART_OVERSAMPLING_16;

  if (HAL_UART_Init(&huart1) != HAL_OK)
  {
    Error_Handler();
  }
}
```

UART1 □□□　　□ Titania □□□□□□

UART1 □□□□□

□□	□	□□
`BaudRate`	9600	□□□□
`WordLength`	8B	8 □□□□
`StopBits`	1	1 □□□□□
`Parity`	NONE	□□□□□

2?? **UART1 ????** (Titania ????)

```
// □□□□□□□
uint8_t sendData_lenght = 84;        //□□□□□□□□
uint8_t Send_uart[87];              // sendData_lenght + 3 (84 + 3)
uint32_t last_uart_tick = 0;        // □□□□
uint8_t Send_head[3] = {0x02, 0x00, 0x3A};  // □□□□
uint8_t Send_checksum = 0;

// □□□□□□□□□□
while (1)
{
    if (HAL_GetTick() - last_uart_tick >= 300)
    {
        // □□□□□
        Send_uart[0] = Send_head[0];   // 0x02
        Send_uart[1] = Send_head[1];   // 0x00
        Send_uart[2] = Send_head[2];   // 0x3A

        Send_uart[86]++;               // ← □□□□□□□□ byte□

        // □□ 87 bytes□□ 3 bytes + 84 bytes □□□
        HAL_UART_Transmit(&huart1, Send_uart, sendData_lenght + 3, 1000);

        last_uart_tick = HAL_GetTick();  // □□□□□□
    }
}
```

Titania □□□□□□□□□

| □ (3B) | □□ (84B) |

□□□□□ □ 300ms □□□□

3?? **UART6 ???** (??/????)

```
static void MX_USART6_UART_Init(void)
{
  huart6.Instance = USART6;
  huart6.Init.BaudRate = 115200;             // ← □□□□115200 bps□□□□
  huart6.Init.WordLength = UART_WORDLENGTH_8B;
  huart6.Init.StopBits = UART_STOPBITS_1;
  huart6.Init.Parity = UART_PARITY_NONE;
  huart6.Init.Mode = UART_MODE_TX_RX;
  huart6.Init.HwFlowCtl = UART_HWCONTROL_NONE;
  huart6.Init.OverSampling = UART_OVERSAMPLING_16;

  if (HAL_UART_Init(&huart6) != HAL_OK)
  {
    Error_Handler();
  }
}
```

UART6 □□□ IMU

UART6 □□□□□

□□	□	□□
`BaudRate`	115200	□□□□
`WordLength`	8B	8 □□□□
`StopBits`	1	1 □□□□□
`Parity`	NONE	□□□□□

???????

? **CAN ??????**

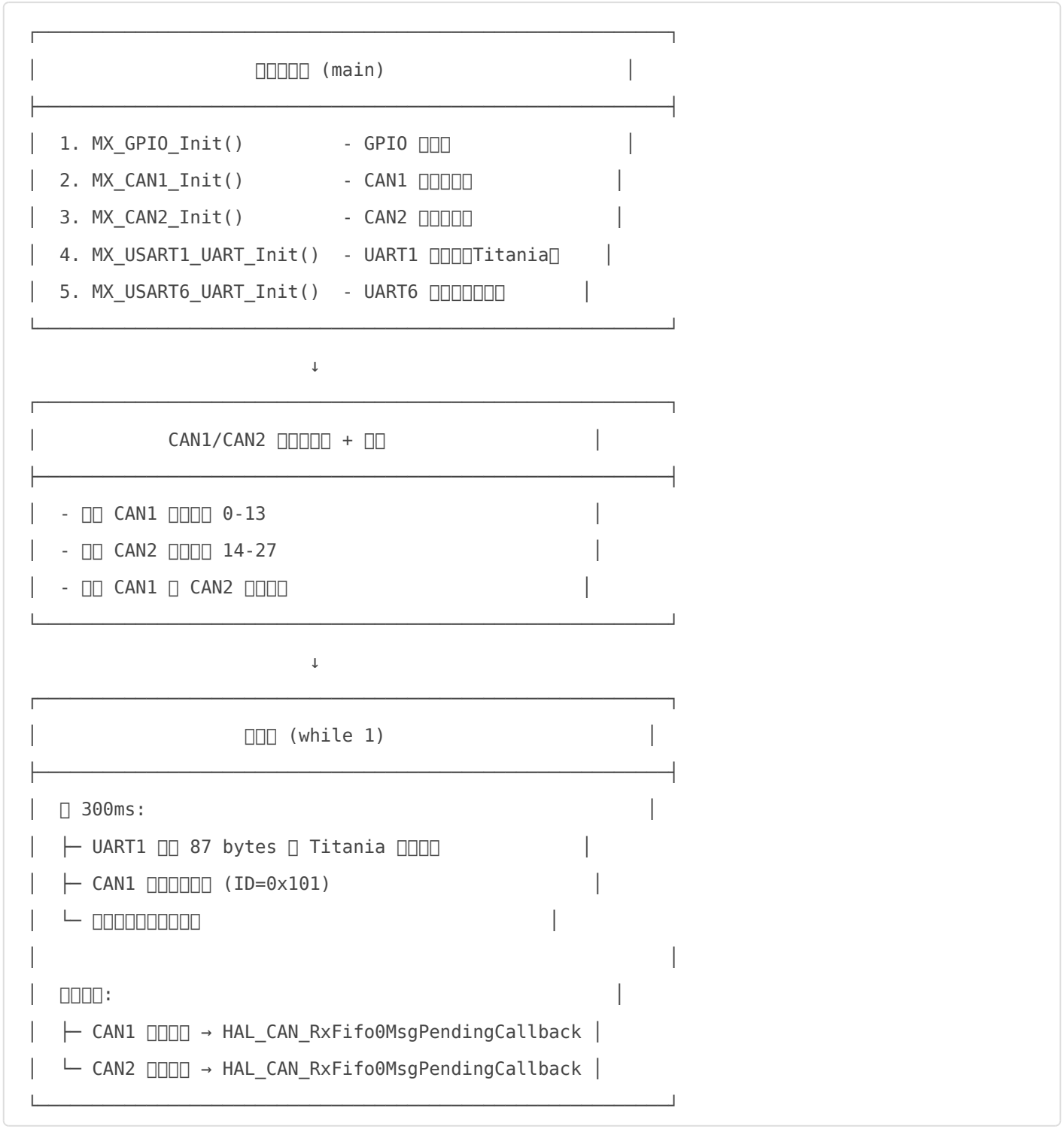
```
void HAL_CAN_RxFifo0MsgPendingCallback(CAN_HandleTypeDef *hcan)
{
    CAN_Counter++;  // □□□□□□□□□

    if (hcan->Instance == CAN1)
    {
        // CAN1 □□□□□□□
        if (HAL_CAN_GetRxMessage(hcan, CAN_RX_FIFO0, &RxHeader1, RxData1) == HAL_OK)
        {
            // □□□□ ID
            if (RxHeader1.StdId == 0x101)
            {
                // □□ CAN1 ID=0x101 □□□
                // RxData1[0] ~ RxData1[7]□8 bytes □□
            }
        }
    }
    else if (hcan->Instance == CAN2)
    {
        // CAN2 □□□□□□□
        if (HAL_CAN_GetRxMessage(hcan, CAN_RX_FIFO0, &RxHeader2, RxData2) == HAL_OK)
        {
            // □ RxData2 □□□□□□□□□4 bytes□
            // □□□□□RxData2[3] (LSB) → RxData2[6] (MSB)
            raw_encoder_value = (uint32_t)(
                (RxData2[6] << 24) |    // □□□ byte
                (RxData2[5] << 16) |
                (RxData2[4] << 8)  |
                RxData2[3]              // □□□ byte
            );
        }
    }
}
```

□□□□□

1. □□□□ □ CAN FIFO0 □□□□□□□□□
2. □□□□ □□□ `hcan->Instance` □□ CAN1 □ CAN2
3. □□□□ □□□ `HAL_CAN_GetRxMessage()` □□□□□□□
4. □□□□ □□□□□ ID □□□□□□

????

? ???????


□□□□□□ (main)
1. MX_GPIO_Init() - GPIO □□□
2. MX_CAN1_Init() - CAN1 □□□□□□
3. MX_CAN2_Init() - CAN2 □□□□□□
4. MX_USART1_UART_Init() - UART1 □□□□Titania□
5. MX_USART6_UART_Init() - UART6 □□□□□□□□
CAN1/CAN2 □□□□□□ + □□
- □□ CAN1 □□□□□ 0-13
- □□ CAN2 □□□□□ 14-27
- □□ CAN1 □ CAN2 □□□□□
□□□ (while 1)
□ 300ms:
├─ UART1 □□ 87 bytes □ Titania □□□□
├─ CAN1 □□□□□□□ (ID=0x101)
└─ □□□□□□□□□□□□
□□□□:
├─ CAN1 □□□□□ → HAL_CAN_RxFifo0MsgPendingCallback
└─ CAN2 □□□□□ → HAL_CAN_RxFifo0MsgPendingCallback

? ??????

CAN1 ??

- [ ] `hcan1.Instance = CAN1`
- [ ] `Prescaler = 5` □□□□ 500kbps□
- [ ] `FilterBank = 0` □□□□ 0 □□□□□
- [ ] `FilterFIFOAssignment = CAN_RX_FIFO0`
- [ ] □□□ `HAL_CAN_ActivateNotification(&hcan1, CAN_IT_RX_FIFO0_MSG_PENDING)`

CAN2 ??

- [ ] `hcan2.Instance = CAN2`
- [ ] `Prescaler = 5` □□□□ 500kbps□
- [ ] `FilterBank = 14` □ Slave □□□□
- [ ] `FilterFIFOAssignment = CAN_RX_FIFO0`
- [ ] □□□ `HAL_CAN_ActivateNotification(&hcan2, CAN_IT_RX_FIFO0_MSG_PENDING)`

UART1 ???Titania?

- [ ] `BaudRate = 9600`
- [ ] □□□□□ (3B) + □□ (84B) = 87 bytes
- [ ] □□□□ 300ms
- [ ] □□□ `Send_uart[86]`

UART6 ??????

- [ ] `BaudRate = 115200`
- [ ] □□□□□□□

? ?????

□□	CAN1	CAN2	UART1	UART6
□□	CAN1	CAN2	USART1	USART6
□□□	500kbps	500kbps	9600	115200
□□□	Bank 0-13	Bank 14-27	-	-
□□	□□□□	□□□ /□□□	□□□□	□□
□□	FIFO0	FIFO0	-	-

□□	CAN1	CAN2	UART1	UART6
□□□□	`CAN1_Send_Test()`	-	`HAL_UART_Transmit()`	`HAL_UART_Transmit()`

? ????

????

Q1: □□□□ CAN □□□□

```
// □□ Prescaler □□
// CAN_BaudRate = 45MHz / (Prescaler × 18)
// □□□ 250kbps□Prescaler = 10
hcan1.Init.Prescaler = 10;  // 250 kbps
```

Q2: □□□□□□ CAN ID □□□

```
// □□□□□□□
sFilterConfig.FilterIdHigh = 0x2020;    // ID □□
sFilterConfig.FilterIdLow = 0x0000;     // ID □□
sFilterConfig.FilterMaskIdHigh = 0xFFFF; // □□□
sFilterConfig.FilterMaskIdLow = 0x0000;  // □□□
```

Q3: UART □□□□□□□

```
// □□□□□□□□□□□□□□□
HAL_UART_Transmit(&huart1, Send_uart, 87, 5000);  // 5□□□

// □□□ DMA □□
HAL_UART_Transmit_DMA(&huart1, Send_uart, 87);
```

□□□□ 1.0
□□□□ 2026□ 3□ 5□
□□□□ STM32F4 □□

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